1. (normative)  
   Verification programme - DRD
   1. DRD identification
      1. Requirement identification and source document

This DRD is called from ECSS-Q-ST-70-61, requirement 13.1.4a.

* + 1. Purpose and objective

The purpose of the Verification programme DRD is to detail the requirements for the documentation of the verification programme.

* 1. Expected response
     1. Scope and content

The verification programme documentation shall contain as a minimum the following:

Indication of process of the assembly:

soldering process

repair process.

Substrate information:

PCB material and manufacturer

PCB footprint surface finish

Number of layers

Thickness

Build up with identification of signal and full copper plane

Connection of the footprints to the internal layer representative of the FM

Location of the components on the PCB

For through hole component, ratio of hole to lead diameter

Location of the mechanical fixation or stiffeners if any

Number of PCB used for the verification programme.

Materials used

Solder paste and wire designation, commercial trademark, and composition with associated flux class

Flux class used for pretinning and soldering

Conformal coating

Adhesive, potting, underfill and encapsulants used for mechanical and for thermal purpose

Solvent

Others.

List of components with their materials leads and finish

Environmental test conditions and facility

Electrical continuity test specification, procedure

Verification method Microsection or Electrical monitoring

Microsection laboratory.

PID and Manufacturing document process references

Verification workflow

Verification by similarity

Certification status of the operators and inspectors

Compliance status of the operators and inspectors

Compliance of the manufacturing room

Additional information.

The content of Verification programme specified in requirement A.2.1a may be tailored for companies already having an approved PID in accordance with this standard.

* + 1. Special remarks

None.