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Space engineering

SpaceWire - Remote memory access protocol

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Foreword

This Standard is one of the series of ECSS Standards intended to be applied together for the management, engineering and product assurance in space projects and applications. ECSS is a cooperative effort of the European Space Agency, national space agencies and European industry associations for the purpose of developing and maintaining common standards. Requirements in this Standard are defined in terms of what shall be accomplished, rather than in terms of how to organize and perform the necessary work. This allows existing organizational structures and methods to be applied where they are effective, and for the structures and methods to evolve as necessary without rewriting the standards.

This Standard has been prepared by the ECSS-E-ST-50-52 Working Group, reviewed by the ECSS Executive Secretariat and approved by the ECSS Technical Authority.

Disclaimer

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1 Scope

There is a number of communication protocols that can be used in conjunction with the SpaceWire Standard (ECSS-E-ST-50-12), to provide a comprehensive set of services for onboard user applications. To distinguish between the various protocols a protocol identifier is used, as specified in ECSS-E-ST-50-51.

This Standard specifies the Remote Memory Access protocol (RMAP), which is one of these protocols that works over SpaceWire.

The aim of RMAP is to support reading from and writing to memory in a remote SpaceWire node. RMAP can be used to configure a SpaceWire network, control SpaceWire nodes, and to transfer data to and from SpaceWire nodes. RMAP is specified in this Standard.

This standard may be tailored for the specific characteristic and constrains of a space project in conformance with ECSS-S-ST-00.

2 Normative references

The following normative documents contain provisions which, through reference in this text, constitute provisions of this ECSS Standard. For dated references, subsequent amendments to, or revision of any of these publications do not apply. However, parties to agreements based on this ECSS Standard are encouraged to investigate the possibility of applying the more recent editions of the normative documents indicated below. For undated references, the latest edition of the publication referred to applies.

ECSS-S-ST-00-01	ECSS system - Glossary of terms
ECSS-E-ST-50-12	Space engineering - SpaceWire - Links, nodes, routers and networks
ECSS-E-ST-50-51	Space engineering - SpaceWire protocol identification

3 Terms, definitions and abbreviated terms

3.1 Terms defined in other standards

For the purpose of this Standard, the terms and definitions from ECSS-S-ST-00-01 and ECSS-E-ST-50-51 apply.

3.2 Terms specific to the present standard

None.

3.3 Abbreviated terms

The following abbreviations are defined and used within this standard:

Abbreviation	**Meaning**
CRC	cyclic redundancy code
EEP	error end of packet
EOP	end of packet
FCT	flow control token
FIFO	first in first out
ID	identifier
inc	increment
Len	length
LS	least-significant
LSB	least-significant bit
MS	most-significant
MSB	most-significant bit
RMAP	remote memory access protocol
RMW	read-modify-write
SOIS	spacecraft onboard interface services
SpW	SpaceWire
SSNSAP	source subnetwork service access point

VHDL	vhsic hardware description language
VHSIC	very high speed integrated circuit

3.4 Conventions

In this document hexadecimal numbers are written with the prefix 0x, for example 0x34 and 0xDF15.

Binary numbers are written with the prefix 0b, for example 0b01001100 and 0b01.

Decimal numbers have no prefix.

4 Principles

4.1 Remote Memory Access Protocol (RMAP) purpose

The aim of RMAP is to support reading from and writing to memory in a remote SpaceWire node. RMAP can be used to configure a SpaceWire network, control SpaceWire nodes, and to transfer data to and from SpaceWire nodes. RMAP is specified in this Standard.

The remote memory access protocol (RMAP) has been designed to support a wide range of SpaceWire applications. Its primary purposes however are to configure a SpaceWire network, to control SpaceWire nodes and to gather data and status information from those nodes. RMAP can operate alongside other communication protocols running over SpaceWire.

RMAP can be used to configure SpaceWire routing switches, setting their operating parameters and routing table information. It can also be used to monitor the status of those routing switches. RMAP can be used to configure and read the status of nodes on the SpaceWire network. For example, the operating data rate of a node can be set to 100 Mbits/s and the interface can be set to auto-start mode. RMAP can also be used to download and debug software on a remote processor.

For simple SpaceWire units without an embedded processor, RMAP can be used to set application configuration registers, to read status information and to read from or write data to memory in the unit.

For intelligent SpaceWire units RMAP can provide the basis for a wide range of communication services. Configuration, status gathering and data transfer to and from memory or mailboxes can be supported.

4.2 Guide to clause 5

Specification of the fields used in RMAP commands and replies is given in clause 5. The CRC used by RMAP is specified in clause 5.2. The write command is defined in clause 5.3, the read command in clause 5.4 and the read-modify-write command in clause 5.5. The error codes that are used in RMAP replies are listed in clause 5.6. The way in which partial implementations of RMAP can be implemented is described in clause 5.7. Clause 5.8 specifies the conformance statements i.e. clauses that are implemented and the ancillary information that is provided, in order for a supplier to claim conformance to the SpaceWire RMAP standard. Example VHDL and C-code for the 8-bit CRC used by RMAP is given in Annex A.

4.3 RMAP operations

4.3.1 Introduction

RMAP is used to write to and read from memory, registers, FIFO memory, mailboxes, etc, in a target on a SpaceWire network. Input/output registers, control/status registers and FIFOs are memory-mapped and therefore are accessed as memory. Mailboxes are indirect memory areas that are referenced using a memory address.

All read and write operations defined in the RMAP protocol are posted operations i.e. the initiator does not wait for a reply to be received. This means that many read and write commands can be outstanding at any time. There is no timeout mechanism implemented in RMAP for missing replies. If a reply timeout mechanism is used, it is implemented in the initiator user application.

4.3.2 Write commands

The write command provides a means for one node, the initiator, to write zero or more bytes of data into a specified area of memory in another node, the target on a SpaceWire network.

Write commands can be acknowledged or not acknowledged by the target when they have been received correctly. If the write command is acknowledged and there is an error with the write command, the target replies with an error/status code to the initiator (or other node) that sent the command. The error/status code can only be sent to the initiator if the write command header was received intact, so that a target that detected an error knows where to send the reply. If no reply is requested then the fact that an error occurred can be stored in a status register in the target.

Write commands can perform the write operation after verifying that the data has been transferred to the target without error, or it can write the data without verification. Verification on the data can be performed only by buffering in the target to store the data while it is being verified, before it is written. The amount of buffering is likely to be limited so verified writes can only be performed for a relatively small amount of data that fits into the available buffer at the target. Verified writes are normally used when writing to configuration or control registers. Larger amounts of data can be written but without verification prior to writing. Verification in this case is done after the data has been written.

The acknowledged/non-acknowledged and verified/non-verified options to the write command result in four different write operations:

- **Write non-acknowledged, non-verified** - writes zero or more bytes to memory in a target. The command header is checked using a CRC before the data is written, but the data itself is not checked before it is written. No reply is sent to the initiator of the write command. This command is typically used for writing large amounts of data to a target where it can be safely assumed that the write operation completed successfully. For example the writing of camera data to a temporary working buffer.

- **Write non-acknowledged, verified** - writes zero or more bytes to memory in a target. Both the command header and data are checked using CRCs before the data is written. This limits the amount of data that can be transferred in a single write operation, but writing erroneous data to memory is unlikely. No reply is sent to the initiator of the write command. This command is typically used for writing command registers and small amounts of data to a target where it can be safely assumed that the write operation completed successfully. For example writing many commands to different configuration registers in a device and then checking for an error using a status register.
- **Write acknowledged, non-verified** - writes zero or more bytes to memory in a target. The command header is checked using a CRC before the data is written, but the data itself is not checked before it is written. A reply to indicate the command execution status is sent to the initiator of the write command. This command is typically used for writing large amounts of data to a target where it can be safely assumed that the write operation completed successfully, but an acknowledgement is required. For example writing sensor data to memory.
- **Write acknowledged, verified** - writes zero or more bytes to memory in a target. Both the command header and data are checked using CRCs before the data is written. This limits the amount of data that can be transferred in a single write operation, but writing erroneous data to memory is unlikely. A reply to indicate the command execution status is sent to the initiator of the write command. This command is typically used for writing small amounts of data to a target where it is important to have confirmation that the write operation was executed successfully. For example writing to configuration registers.

4.3.3 Read commands

The read command provides a means for one node, the initiator, to read zero or more bytes of data from a specified area of memory in another node, the target on a SpaceWire network. The data read is returned in a reply packet which normally goes back to the initiator.

4.3.4 Read-modify-write

The read-modify-write command provides a means for one node, the initiator, to read a memory location in another node, the target, modify the value read in some way and then write the new value back to the same memory location. The original value read from memory is returned in a reply packet to the initiator.

5 Requirements

5.1 RMAP command and reply fields

5.1.1 Target SpaceWire Address field

a. The Target SpaceWire Address field shall comprise zero or more data characters forming the SpaceWire address which is used to route the command to the target.

NOTE The Target SpaceWire Address is stripped off by the time the packet reaches the target.

b. SpaceWire path addressing and regional addressing may be used.

c. The Target SpaceWire Address field shall not be used when a single logical address is being used for routing the command to the target.

NOTE In this case the command is routed to the target by the Target Logical Address contained in the Target Logical Address field.

5.1.2 Target Logical Address field

a. Target Logical Address field shall be an 8-bit field that contains a logical address of the target.

NOTE 1 The Target Logical Address field is normally set to a logical address recognised by the target.

NOTE 2 If the target does not have a specific logical address then the Target Logical Address field can be set to the default value 254 (0xFE).

NOTE 3 A target can have more than one logical address.

5.1.3 Protocol Identifier field

a. The Protocol Identifier field shall be an 8-bit field that contains the Protocol Identifier.

b. The Protocol Identifier field shall be set to the value 1 (0x01) which is the Protocol Identifier for the Remote Memory Access Protocol.

5.1.4 Instruction field

5.1.4.1 General

a. The Instruction field shall be an 8-bit composite field that comprises the packet type, command and Reply Address length fields.

5.1.4.2 Packet type field

a. The Packet Type field shall be a 2-bit field that determines the type of RMAP packet i.e. a command (0b01) or reply (0b00).

b. The other possible values (0b10 and 0b11) of the packet type field are reserved.

5.1.4.3 Command field

a. Command field shall be:

1. A 4-bit field in an RMAP command that specifies the type of command, or

2. A 4-bit field in an RMAP reply that specifies the type of command that caused the reply.

b. The command codes shall have the meanings listed in Table 5-1.

Table 5-1: RMAP Command Codes

Bit 5	**Bit 4**	**Bit 3**	**Bit 2**	**Command Field**
Write/ Read	**Verify Data Before Write**	**Reply**	**Increment Address**	**Function**
0	0	0	0	Invalid
0	0	0	1	Invalid
0	0	1	0	Read single address
0	0	1	1	Read incrementing addresses
0	1	0	0	Invalid
0	1	0	1	Invalid
0	1	1	0	Invalid
0	1	1	1	Read-Modify-Write incrementing addresses
1	0	0	0	Write, single address, don’t verify before writing, no reply
1	0	0	1	Write, incrementing addresses, don’t verify before writing, no reply
1	0	1	0	Write, single address, don’t verify before writing, send reply

Bit 5	Bit 4	Bit 3	Bit 2	Command Field
1	0	1	1	Write, incrementing addresses, don't verify before writing, send reply
1	1	0	0	Write, single address, verify before writing, no reply
1	1	0	1	Write, incrementing addresses, verify before writing, no reply
1	1	1	0	Write, single address, verify before writing, send reply
1	1	1	1	Write, incrementing addresses, verify before writing, send reply

5.1.4.4 Reply Address length field

a. The Reply Address Length field shall be:

1. A 2-bit field in an RMAP command that determines the number of bytes in the Reply Address field of a command.
2. A 2-bit field in an RMAP reply that is a copy of the 2-bit Reply Address Length field in the command that caused the reply.

5.1.5 Key field

a. The Key field shall be an 8-bit field that contains a key which is matched by the target user application in order for the RMAP command to be authorised.

NOTE The Key is only used for command authorisation. It is not used for other purposes.

5.1.6 Reply Address field

a. The Reply Address field shall be a 0, 4, 8 or 12-byte field in a command that contains the SpaceWire address for the reply to the command.

b. The size of the Reply Address field shall depend on the value of the Reply Address Length field as detailed in Table 5-2.

NOTE The Reply Address is not needed if logical addressing is being used. The Reply Address is normally used by the target to send replies or data back to the initiator that requested a write or read operation using path addressing. The Reply Address allows path addressing and regional logical addressing to be used to specify the node that is to receive the reply (normally the initiator).

Table 5-2: Reply Address field size

Value of Reply Address Length Field	Size of Reply Address field
0b00	0
0b01	4 bytes
0b10	8 bytes
0b11	12 bytes

c. Leading bytes with the value 0x00 in the Reply Address field shall be ignored.

d. If the Reply Address Length field is not zero and the Reply Address bytes are all zero (0x00), a single zero value data character shall be sent as part of the Reply SpaceWire Address field.

NOTE This is so that a Reply SpaceWire Address comprising a single zero (0x00) data character is possible.

e. Any characters in the Reply Address field after the leading bytes with the value 0x00 shall form the Reply SpaceWire Address.

f. SpaceWire path addressing and regional addressing shall be used to form the Reply Address field.

g. Some examples of the mapping between the contents of the Reply Address field and the Reply SpaceWire Address are listed in Table 5-3.

Table 5-3: Example Reply Address field to Reply SpaceWire Address mappings

Reply Address field	Resulting Reply SpaceWire Address
0x00 0x00 0x00 0x00	0x00
0x00 0x00 0x01 0x02	0x01 0x02
0x00 0x01 0x00 0x02	0x01 0x00 0x02
0x00 0x01 0x02 0x00	0x01 0x02 0x00
0x00 0x00 0x00 0x01 0x02 0x03 0x04 0x05	0x01 0x02 0x03 0x04 0x05
0x00 0x00 0x66 0x05	0x66 0x05
0x00 0x54 0x08 0x00	0x54 0x08 0x00

h. The Reply Address field shall not be used when a single logical address is used for routing the reply to its initiator (or other node).

NOTE In this case the reply is routed to the initiator by the Initiator Logical Address.

i. An RMAP implementation may use an implicit return address or implicit partial return address.

NOTE For example, a SpaceWire router with an RMAP configuration port can automatically route the reply to an RMAP command out of the same port that the RMAP command arrived on without the need for this being explicitly specified in the Reply Address field.

5.1.7 Initiator Logical Address field

a. The Initiator Logical Address field shall be an 8-bit field that contains either:

1. The logical address of the initiator of a command packet, if the initiator has a logical address, or
2. 254 (0xFE) otherwise.

NOTE 1 The value 254 (0xFE) is the default logical address (see ECSS-E-ST-50-51, Clause 5.2.1).

NOTE 2 An initiator can have more than one logical address.

5.1.8 Transaction Identifier field

a. The Transaction Identifier field shall be a 16-bit field used to associate replies with the command that caused the reply.

b. The Transaction Identifier in a reply shall have the same value as the Transaction Identifier in the command that caused the reply.

c. The most significant byte of the Transaction Identifier shall be sent first.

NOTE Typically Transaction Identifiers are an incrementing integer sequence, with each successive RMAP transaction being given the next number in the sequence. The intention of the Transaction Identifier is to uniquely identify a transaction.

5.1.9 Extended Address field

a. The Extended Address field shall be an 8-bit field that contains the most-significant 8-bits of the memory address extending the 32-bit memory address to 40-bits.

5.1.10 Address field

a. The Address field shall be a 32-bit field that contains the least-significant 32-bits of the memory address.

b. The most significant byte of the Address field shall be sent first.

5.1.11 Data Length field

a. The Data Length field shall be a 24-bit field that contains the length in bytes of the data field or data and mask field in a command or reply.

b. The most significant byte of the Data Length field shall be sent first.

5.1.12 Header CRC field

a. The Header CRC field shall be an 8-bit field that contains an 8-bit Cyclic Redundancy Code (CRC) covering each byte in the header, starting with the Target Logical Address and ending with the byte before the Header CRC in a command and starting with the Initiator Logical Address and ending with the byte before the Header CRC in a reply.

5.1.13 Data field

a. The Data field shall be a variable length field containing the data bytes that are written in a write command or the data bytes that are read in a read reply, or read and written in a read-modify-write command and reply.

NOTE The order of the bytes in the data field is up to the specific implementation and is defined in the target product characteristic table (see clause 5.8).

5.1.14 Mask field

a. The Mask field shall be a variable length field containing the mask in a read-modify-write command.

5.1.15 Data CRC field

a. The Data CRC field shall be an 8-bit field that contains an 8-bit Cyclic Redundancy Code (CRC) covering each byte in the data and mask field starting with the byte after the Header CRC and ending with the byte before the Data CRC.

5.1.16 Reply SpaceWire Address field

a. The Reply SpaceWire Address field shall be a variable length field formed from the contents of the Reply Address field of a command which is used to route a reply back to the initiator or other intended destination for the reply.

5.1.17 Status field

a. The Status field shall be an 8-bit field in a reply containing a status/error code as defined in clause 5.6.

5.2 Cyclic Redundancy Code

a. The same method of calculating the CRC shall be used for both the Header CRC and the Data CRC.

b. The CRC calculation procedures shall:

1. use modulo 2 arithmetic for polynomial coefficients;

2. use a systematic binary $(n+8, n)$ block code, where $(n+8)$ is the number of bits of the codeword $c(x)$ and n is divisible by 8; n is the number of bits covered by the CRC;

3. use the following generating polynomial:

$$g(x) = x^8 + x^2 + x + 1$$

4. use byte format as input and output, for which the bits are represented as:

$$b_7\, b_6\, b_5\, b_4\, b_3\, b_2\, b_1\, b_0$$

where b_7 is the most significant bit and b_0 is the least significant bit;

c. The CRC generation procedure shall behave as follows:

1. The procedure accepts an *n*-bit input which is used to construct $m(x)$, where:

(a) the *n*-bit input is defined to be the set of bits $B_{i,j}$ grouped into $n/8$ bytes where $i = \{0, 1, \ldots, n/8-1\}$ is the byte index and $j = \{7, 6, \ldots, 0\}$ is the bit index;

(b) the $n/8$ input bytes correspond to the RMAP fields covered by the CRC excluding the CRC byte; the first byte transmitted has index $i = 0$; the last byte transmitted has index $i = n/8-1$;

(c) $m(x)$ is a polynomial $m_{n-1}x^{n-1} + m_{n-1}x^{n-2} + \ldots + m_0 x^0$ having binary coefficients m_i;

(d) $m(x)$ can be represented as an *n*-bit vector where coefficient m_{n-1} of the highest power of x is the most significant bit and coefficient m_0 of the lowest power of x is the least significant bit;

(e) the bit vector representation of $m(x)$ is formed by concatenating the $n/8$ bytes of the input in transmission order, where the least significant bit b_0 of each byte is taken first and the most significant bit b_7 of each byte is taken last:

$$m_{n-1} = B_{0,0}, m_{n-2} = B_{0,1}, m_{n-3} = B_{0,2}, \ldots, m_{n-7} = B_{0,6}, m_{n-8} = B_{0,7},$$
$$m_{n-9} = B_{1,0}, m_{n-10} = B_{1,1}, m_{n-11} = B_{1,2}, \ldots, m_{n-15} = B_{1,6}, m_{n-16} = B_{1,7}, \ldots$$
$$m_7 = B_{n/8-1,0}, m_6 = B_{n/8-1,1}, m_5 = B_{n/8-1,2}, \ldots, m_1 = B_{n/8-1,6}, m_0 = B_{n/8-1,7}$$

2. The procedure generates the remainder polynomial $r(x)$ given by the equation:

$$r(x) = \left[m(x) \cdot x^8 \right] \text{modulo } g(x)$$

where $r(x) = r_7 x^7 + r_6 x^6 + \ldots r_0 x^0$ and r_i are binary coefficients;

3. The Header and Data CRC are formed from the 8-bit vector representation of $r(x)$; the least significant bit b_0 of the CRC byte is coefficient r_7 of the highest power of x, while the most significant bit b_7 of the CRC byte is coefficient r_0 of the lowest power of x:

$$b_7 = r_0, b_6 = r_1, b_5 = r_2, b_4 = r_3, b_3 = r_4, b_2 = r_5, b_1 = r_6, b_0 = r_7$$

NOTE 1 The codeword $c(x) = m(x) \cdot x^8 + r(x)$ is formed by concatenating the bit vector representations of $m(x)$ and $r(x)$.

NOTE 2 When a Galois version of a Linear Feedback Shift Register is used for CRC generation, its initial value is zero.

NOTE 3 Example VHDL and C-code implementations of this CRC algorithm are included in clause Annex A.

d. If the CRC generation procedure is applied to the bytes covered by the CRC *excluding* the CRC byte then the generated CRC shall be compared directly with the expected CRC byte. If the generated and expected CRC bytes are equal then no errors have been detected; if they are different then an error has been detected.

e. If the CRC generation procedure is applied to the bytes covered by the CRC *including* the CRC byte then the output of the CRC generation procedure shall be zero if no errors have been detected and non-zero if an error has been detected.

NOTE 1 When the codeword $c^*(x)$ is input to the CRC generator then the remainder is the syndrome: $s(x) = \left[c^*(x) \cdot x^8 \right] \text{modulo } g(x)$.

NOTE 2 The codeword $c^*(x)$ is the concatenation of the Header or Data bytes covered by the CRC, followed by the CRC byte.

f. If the value of the data length field is zero, then the Data CRC shall be 0x00.

NOTE Read commands and write replies have no Data CRC field.

g. The CRC shall be calculated on the byte stream not the serial bit stream, since the RMAP protocol operates above the SpaceWire packet level as specified in ECSS-E-ST-50-12.

NOTE 1 The equivalent bit serial version takes the least-significant bit of each byte first and does not include data/control or parity bits, NULL, FCT or other non-data characters.

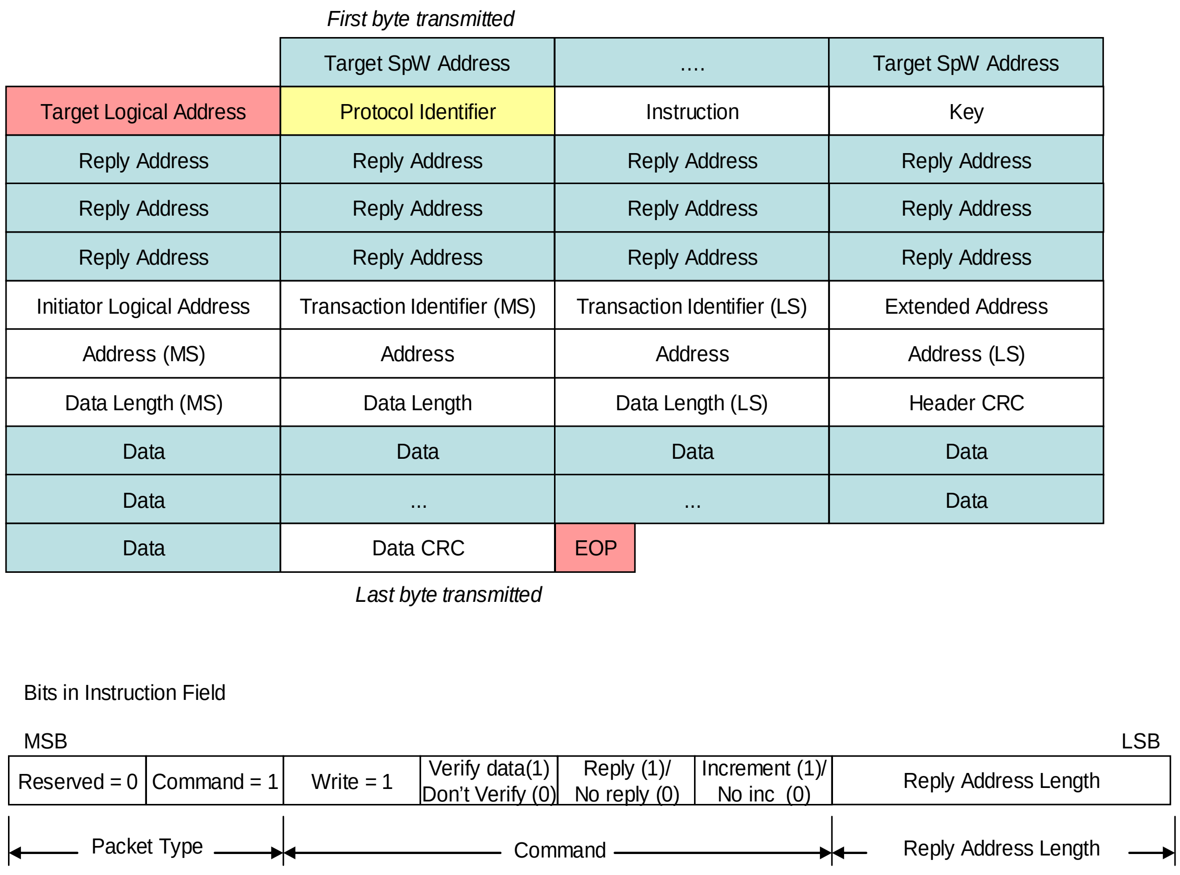
NOTE 2 See clause Annex A for some examples of how the CRC is implemented along with some test patterns.

5.3 Write Command

5.3.1 Write command format

5.3.1.1 Fields

a. The write command shall contain the fields shown in Figure 5-1.



First byte transmitted

	Target SpW Address	....	Target SpW Address
Target Logical Address	Protocol Identifier	Instruction	Key
Reply Address	Reply Address	Reply Address	Reply Address
Reply Address	Reply Address	Reply Address	Reply Address
Reply Address	Reply Address	Reply Address	Reply Address
Initiator Logical Address	Transaction Identifier (MS)	Transaction Identifier (LS)	Extended Address
Address (MS)	Address	Address	Address (LS)
Data Length (MS)	Data Length	Data Length (LS)	Header CRC
Data	Data	Data	Data
Data	...	...	Data
Data	Data CRC	EOP	

Last byte transmitted

Bits in Instruction Field

MSB LSB

Reserved = 0	Command = 1	Write = 1	Verify data(1) Don't Verify (0)	Reply (1)/ No reply (0)	Increment (1)/ No inc (0)	Reply Address Length

Figure 5-1: Write Command format

5.3.1.2 Target SpaceWire Address field

a. The Target SpaceWire Address field shall be as defined in clause 5.1.1.

5.3.1.3 Target Logical Address field

a. The Target Logical Address field shall be as defined in clause 5.1.2.

5.3.1.4 Protocol Identifier field

a. The Protocol Identifier field shall be as defined in clause 5.1.3.

5.3.1.5 Instruction field

5.3.1.5.1 Instruction field format

a. The Instruction field format shall be as defined in clause 5.1.4.

5.3.1.5.2 Packet type field

a. The Packet Type field shall be 0b01 to indicate that this is a command.

5.3.1.5.3 Command field

a. The Write/Read bit shall be set (1) for a write command.

b. The Verify-Data-Before-Write bit shall be:

   1. Set (1) if the data is to be checked before it is written to memory, and

   2. Clear (0) otherwise.

c. The Reply bit shall be:

   1. Set (1) if a reply to the write command is required, and

   2. Clear (0) otherwise.

d. The Increment/No increment Address bit shall be:

   1. Set (1) if data is written to sequential memory addresses, and.

   2. Clear (0) if data is written to a single memory address.

5.3.1.5.4 Reply Address length field

a. The Reply Address Length field shall be set to the smallest number of 32-bit words that is able to contain the Reply SpaceWire Address from the target, back to the initiator of the command packet or some other node that is to receive the reply.

   NOTE For example, if three Reply SpaceWire Address bytes are used then the Reply Address Length field is set to one (0b01).

5.3.1.6 Key field

a. The Key field shall be as defined in clause 5.1.5.

5.3.1.7 Reply Address field

a. The Reply Address field shall be as defined in clause 5.1.6.

5.3.1.8 Initiator Logical Address field

a. The Initiator Logical Address field shall be as defined in clause 5.1.7.

5.3.1.9 Transaction Identifier field

a. The Transaction Identifier field format shall be as defined in clause 5.1.8.

5.3.1.10 Extended Address field

a. The Extended Address field shall be as defined in clause 5.1.9.

b. The Extended Address field shall hold the most-significant 8-bits of the starting memory address to be written to.

5.3.1.11 Address field

a. The Address field format shall be as defined in clause 5.1.10.

b. The Address field shall hold the least-significant 32-bits of the starting memory address to which the data in a write command is written.

5.3.1.12 Data Length field

a. The Data Length field format shall be as defined in clause 5.1.11.

NOTE This gives a maximum Data Length of 16 Megabytes -1 in a single write command. If a single byte is being written this field is set to one. If set to zero then no bytes are written to memory which can be used as a test transaction depending upon the implementation.

5.3.1.13 Header CRC field

a. The Header CRC field shall contain an 8-bit CRC as defined in clauses 5.1.12 and 5.2.

5.3.1.14 Data field

a. The Data field shall contain zero or more bytes of data that are written into the memory of the target as defined in clause 5.1.13.

5.3.1.15 Data CRC field

a. The Data CRC shall contain an 8-bit CRC as defined in clauses 5.1.15 and 5.2.

5.3.1.16 EOP character

a. The end of the packet containing the write command shall be indicated by an EOP character.

5.3.2 Write reply format

5.3.2.1 Format

a. The format of the reply to a write command shall contain the fields shown in Figure 5-2.

NOTE A reply is sent by the target back to initiator of the write command or to some other node as defined by the Reply Address field if requested in the write command. The reply indicates the success or failure of the write command by the value in the Status field.

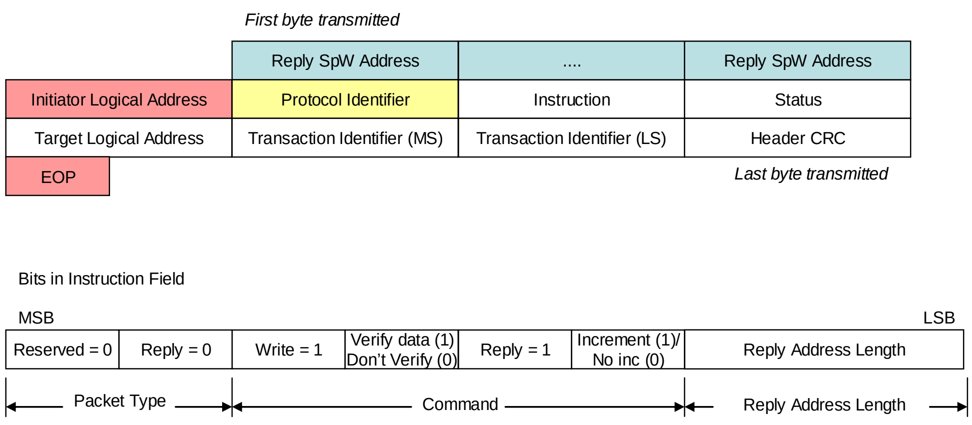


Figure 5-2: Write Reply format

5.3.2.2 Reply SpaceWire Address field

a. The Reply SpaceWire Address field shall comprise zero or more data characters which define how the reply is routed to the initiator or some other node.

b. The SpaceWire address in the Reply SpaceWire Address field shall be constructed from the Reply Address field in the command as detailed in clause 5.1.6.

5.3.2.3 Initiator Logical Address field

a. The Initiator Logical Address field shall be as defined in clause 5.1.7.

5.3.2.4 Protocol Identifier field

a. The Protocol Identifier field shall be as defined in clause 5.1.3.

5.3.2.5 Instruction field

a. The Instruction field format shall be as defined in clause 5.1.4.

b. The Packet Type field shall be 0b00 to indicate that the RMAP packet is a reply.

c. The Command field shall be set to the same value as in the Command field of the write command, clause 5.3.1.5.3.

d. The Reply Address Length field shall be set to the same value as in the Reply Address Length field of the write command, clause 5.3.1.5.4.

5.3.2.6 Status field

a. The Status field format shall be as defined in clause 5.1.17.

b. The Status field shall contain:

   1. 0x00 if the command executed successfully.

   2. A non-zero error code if there was an error with the write command as specified in clause 5.6.

5.3.2.7 Target Logical Address field

a. The Target Logical Address field shall be set to either of:

   1. the value of the Target Logical Address field of the write command, see clause 5.3.1.3, or

   2. a logical address of the target.

      NOTE Normally these are the same.

5.3.2.8 Transaction Identifier field

a. The Transaction Identifier field shall be set to the same value as the Transaction Identifier in the write command, see clause 5.3.1.9.

   NOTE This is so that the initiator of the write command can associate the reply with the original write command.

5.3.2.9 Header CRC field

a. The Header CRC field shall contain an 8-bit CRC as defined in clauses 5.1.12 and 5.2.

5.3.2.10 EOP character

a. The end of the Packet containing the write reply shall be indicated by an EOP character.

5.3.3 Write action

5.3.3.1 Overview

The normal sequence of actions for a write command is illustrated in Figure 5-3.

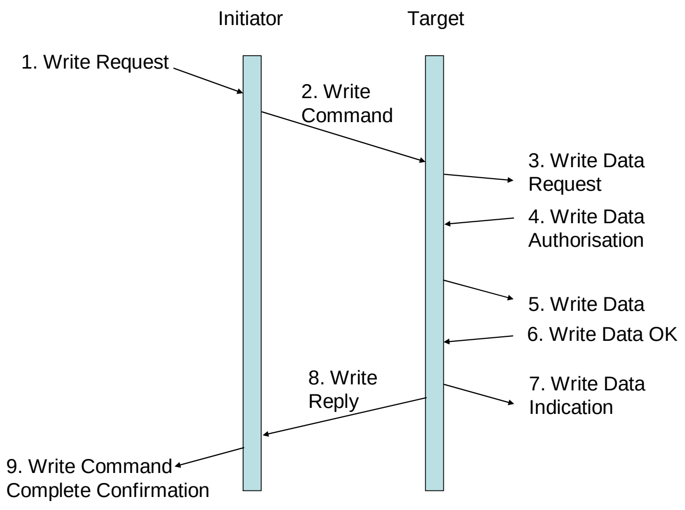


Figure 5-3: Write Command/Reply sequence

5.3.3.2 Write request

a. The write command sequence shall begin when an initiator user application requests to perform a write operation (Write Request).

b. The initiator user application shall pass the following information to the initiator:

1. Target SpaceWire Address
2. Target Logical Address
3. Write command options
4. Key
5. Reply Address (if needed)
6. Initiator Logical Address
7. Transaction Identifier
8. Extended Address
9. Memory address
10. Data Length
11. Data

5.3.3.3 Write command

a. In response to the write request the initiator shall construct the write command including the Header CRC and Data CRC and send it across the SpaceWire network to the target (Write Command).

NOTE The Target SpaceWire Address and Target Logical Address are used to route the command packet to the target.

5.3.3.4 Write data request

5.3.3.4.1 Protocol identifier

a. When a Packet is received at the target and the Protocol Identifier field is 0x01 the packet shall be regarded as an RMAP packet.

5.3.3.4.2 Incomplete header

a. If an EEP or EOP is received before the complete header including header CRC has been received the target shall:

   1. Discard the entire packet,

   2. Not send a reply packet.

b. If an EEP or EOP is received before the complete header including header CRC has been received the target should update the error information to reflect the "EEP" or "Early EOP" error if the target supports error information gathering.

5.3.3.4.3 Error End of Packet

a. If an EEP is received immediately after the complete header including header CRC has been received the target shall:

   1. Discard the entire packet,

   2. Not send a reply packet.

b. If an EEP is received immediately after the complete header including header CRC has been received the target should update the error information to reflect the "EEP" error if the target supports error information gathering.

5.3.3.4.4 Header CRC check

a. When an RMAP packet is received at the target the Header CRC shall be checked.

5.3.3.4.5 Header CRC error

a. When checking the Header CRC indicates an error in the header the target shall:

   1. Discard the entire packet,

   2. Not send a reply packet.

b. When checking the Header CRC indicates an error in the header the target should update the error information to reflect the "Header CRC" error if the target supports error information gathering.

NOTE The sequence of events that occurs when there is a CRC error in the header of the write command is illustrated in Figure 5-4.

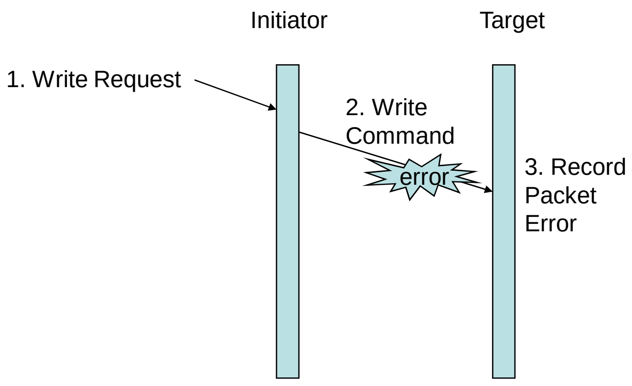


Figure 5-4: Write Command Header Error

5.3.3.4.6 Unused packet type

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11), the target shall:

1. Discard the command packet,
2. Not send a reply.

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11), the target should update the error information to reflect the “unused RMAP packet type or command code” error if the target supports error information gathering.

c. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11), the target may send a reply containing an “unused RMAP packet type or command code” error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields, if a reply has been requested (Reply bit set).

5.3.3.4.7 Invalid command code

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an invalid command code as specified in Table 5-1, the target shall:

1. Discard the command packet,
2. Return an “unused RMAP packet type or command code” error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields, if a reply has been requested (Reply bit set).

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an invalid command code, as specified in Table 5-1 the target should update the error information to reflect the “unused RMAP packet type or command code” error if the target supports error information gathering.

5.3.3.4.8 Write data request action

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains a write command (packet type 0b01 and a write command code) the target shall pass the following information to the target user application:

1. Target Logical Address
2. Instruction
3. Key
4. Initiator Logical Address
5. Transaction Identifier
6. Extended Address
7. Memory address
8. Data Length

5.3.3.5 Write data authorisation

5.3.3.5.1 Write operation authorisation

a. The target user application shall be asked to authorise the write operation.

5.3.3.5.2 Invalid key

a. If the value of the Key is not the value expected by the target user application, the target shall:

1. Discard the command packet,
2. Return an "invalid key" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the value of the Key is not the value expected by the target user application, the target should update the error information to reflect the "invalid key" error if the target supports error information gathering.

5.3.3.5.3 Invalid logical address

a. If the Target Logical Address is not a logical address recognised by the target user application, the target shall:

1. Discard the command packet,
2. Return an "invalid Target Logical Address" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Target Logical Address is not a logical address recognised by the target user application, the target should update the error information to reflect the "invalid Target Logical Address" error if the target supports error information gathering.

5.3.3.5.4 Command rejection

a. If the command is not accepted by the target user application for any other reason, the target shall:

    1. Discard the command packet,

    2. Return a "RMAP command not implemented or not authorised" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the command is not accepted by the target user application for any other reason, the target should update the error information to reflect the "RMAP command not implemented or not authorised" error if the target supports error information gathering.

    NOTE 1 The target user application can reject the command for any reason it likes. For example the address is not 32-bit aligned, the Data Length is not a multiple of 4-bytes, or the address range falls partially or completely outside an acceptable memory address region.

    NOTE 2 The sequence of events that occurs when a write command is not authorised is illustrated in Figure 5-5.

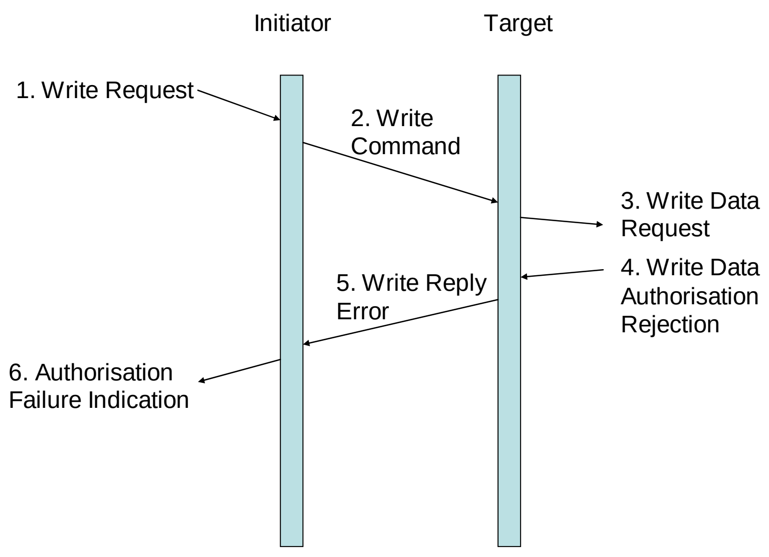


Figure 5-5: Write Data Authorisation Rejection

5.3.3.6 Write data

5.3.3.6.1 Write data action

a. If authorisation is given by the target user application, the data contained in the write command shall be written into the memory location in the target specified by the Extended Address and Address fields (Write Data in Figure 5-3).

5.3.3.6.2 Verify-Data-Before-Write bit set

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header the data shall be buffered and checked using the Data CRC before it is written to memory.

NOTE The size of the Verify-Data-Before-Write data buffer is implementation dependent.

5.3.3.6.3 Buffer space exceeded

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the data exceeds the available buffer space, the target shall:

1. Not write data to memory,
2. Return a "verify buffer overrun" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the data exceeds the available buffer space, the target should update the error information to reflect the "verify buffer overrun" error if the target supports error information gathering.

5.3.3.6.4 Verify-Data-Before-Write action

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the Data CRC is correct and the amount of data matches the value of the data length field, the data shall be written from the buffer into memory.

5.3.3.6.5 Data CRC error

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the Data CRC is in error, the target shall:

1. Not write data to memory,
2. Return an "invalid Data CRC" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the Data CRC is in error, the target should update the error information to reflect the "invalid Data CRC" error if the target supports error information gathering.

5.3.3.6.6 Unexpected EOP

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if there is less data in the data field than specified in the Data Length field of the write command header when the EOP is reached, the target shall:

1. Not write data into memory,
2. Return an "early EOP" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if there is less data in the data field than specified in the Data Length field of the write command header when the EOP is reached, the target should:

1. Indicate that an insufficient data error has occurred to the user application in the target,
2. Update the error information to reflect the insufficient data error if the target supports error information gathering.

5.3.3.6.7 More data than expected

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if there is more data in the data field than specified in the Data Length field of the write command header, the target shall:

1. Not write data into memory,
2. Discard the rest of the packet,
3. Return a "too much data" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if there is more data in the data field than specified in the Data Length field of the write command header, the target should update the error information to reflect "too much data" error if the target supports error information gathering.

5.3.3.6.8 Error End of Packet

a. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the packet ends in an EEP, the target shall:

1. Not write data into memory,
2. Return an "EEP" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is set (1) in the command field of the header and if the packet ends in an EEP, the target should:

1. Indicate that an "EEP" error has occurred to the user application in the target,
2. Update the error information to reflect the "EEP" error if the target supports error information gathering,

5.3.3.6.9 Verify-Data-Before-Write bit not set

a. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header the data shall be written directly to memory without necessarily buffering and checking of the Data CRC before the actual write operation is performed.

5.3.3.6.10 Data CRC error

a. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if there is a Data CRC error the target shall:

1. Update the error information to reflect the "invalid Data CRC" error if the target supports error information gathering,
2. Return an "invalid Data CRC" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

NOTE 1 If verify before write bit is clear (0) then the Data CRC error is reported after the data has been transferred to target memory.

NOTE 2 The sequence of events that occurs when the Data CRC detects an error in the data field is illustrated in Figure 5-6.

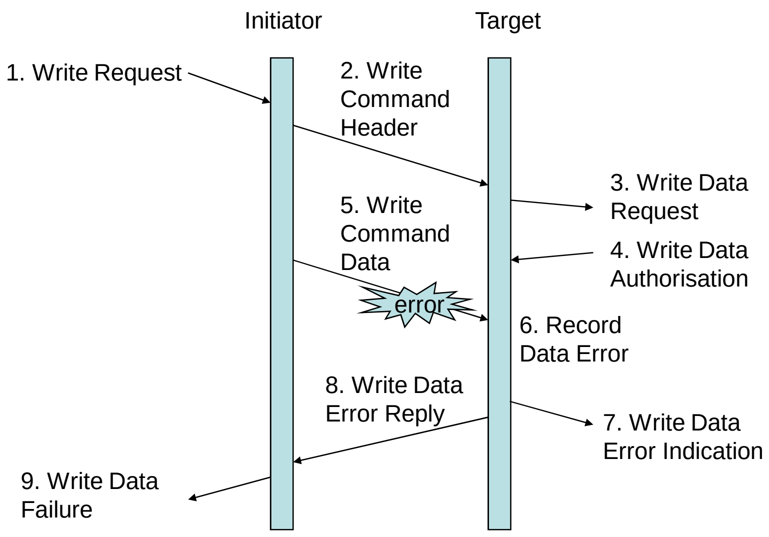


Figure 5-6: Write Command Data Error

5.3.3.6.11 Unexpected EOP

a. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if there is less data in the data field than specified in the Data Length field of the write command header when the EOP is reached, the target shall:

1. Stop transferring into target memory,
2. Return an "early EOP" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if there is less data in the data field than specified in the Data Length field of the write command header when the EOP is reached, the target should:

1. Indicate that an "insufficient data" error has occurred to the user application in the target,

2. Update the error information to reflect the "insufficient data" error if the target supports error information gathering.

NOTE If there is a Data CRC in the packet prior to the EOP then it can be incorrectly transferred into memory at the end of the data.

5.3.3.6.12 More data than expected

a. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if there is more data in the data field than specified in the Data Length field of the write command header, the target shall:

1. Transfer the amount of data specified by the Data Length field of the write command header to memory,

2. Discard the rest of the packet,

3. Return a "too much data" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if there is more data in the data field than specified in the Data Length field of the write command header, the target should update the error information to reflect "too much data" error if the target supports error information gathering.

5.3.3.6.13 Error End of Packet

a. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if the packet ends in an EEP, the target shall:

1. Stop transferring data into target memory,

2. Return an "EEP" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the Verify-Data-Before-Write bit is clear (0) in the command field of the header and if the packet ends in an EEP, the target should:

1. Indicate that an EEP error has occurred to the user application in the target,

2. Update the error information to reflect the "EEP" error if the target supports error information gathering.

5.3.3.6.14 Increment bit

a. If the Increment bit is clear (0) in the command field of the header, the memory address written to in the target shall remain constant i.e. all data in the write command is written to the same memory location.

b. If the Increment bit is set (1) in the command field of the header, the memory address written to in the target shall be incremented as determined by the target user application in order to access sequential memory locations i.e. the data in the write command is written to sequential memory locations.

NOTE The width of the memory locations is determined by the target user application. Byte addressing is not necessarily implied.

5.3.3.7 Write data indication

a. Once data has been written to memory the target user application should be informed that a write operation has taken place (Write Data Indication).

b. If data is not written to memory after authorisation has been given for the write to memory, the target user application should be informed that an error occurred.

5.3.3.8 Write reply

a. If the Reply bit in the command field is set (1) requesting a reply and the write command was executed successfully, the target shall send a reply packet with the status field set to 0x00 indicating that there was no error to the node specified by the Reply Address and Initiator Logical Address fields of the write command (Write Reply).

b. If the Reply bit in the command field is clear (0), the target shall not send a reply.

5.3.3.9 Write command complete confirmation

a. When the write reply is received at the initiator (or other node specified by the Reply Address and Initiator Logical Address), successful completion of the write request or its failure shall be indicated to the user application on that node (Write Complete Confirmation).

b. The Transaction Identifier shall be used to relate the reply to the command that caused the reply.

5.3.3.10 Write not OK

a. If the write operation to memory fails, the target should stop writing to memory as soon as the memory error is detected.

b. If the write operation to memory fails, the target should update the error information to reflect the memory access error if the target supports error information gathering.

c. If the write operation to memory fails, the target shall return a “General” error as specified in clause 5.6 to the node specified in the Reply Address field and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

5.3.3.11 Corrupted write reply

a. If the write reply is corrupted or does not reach the initiator (or other node specified by the Reply Address) intact the initiator shall discard the reply.

b. If the write reply is corrupted or does not reach the initiator (or other node specified by the Reply Address) intact the initiator should:

1. Update the error information to reflect the invalid reply error, if the initiator or other node receiving the invalid reply supports error information gathering,

2. Indicate an error to the user application in the node receiving the reply.

NOTE 1 The sequence of events that occurs when a write reply error occurs is illustrated in Figure 5-7.

NOTE 2 The data has been written into target memory and the target user application has been informed. The initiator application is informed when a write reply is received. It is not informed when no reply is received.

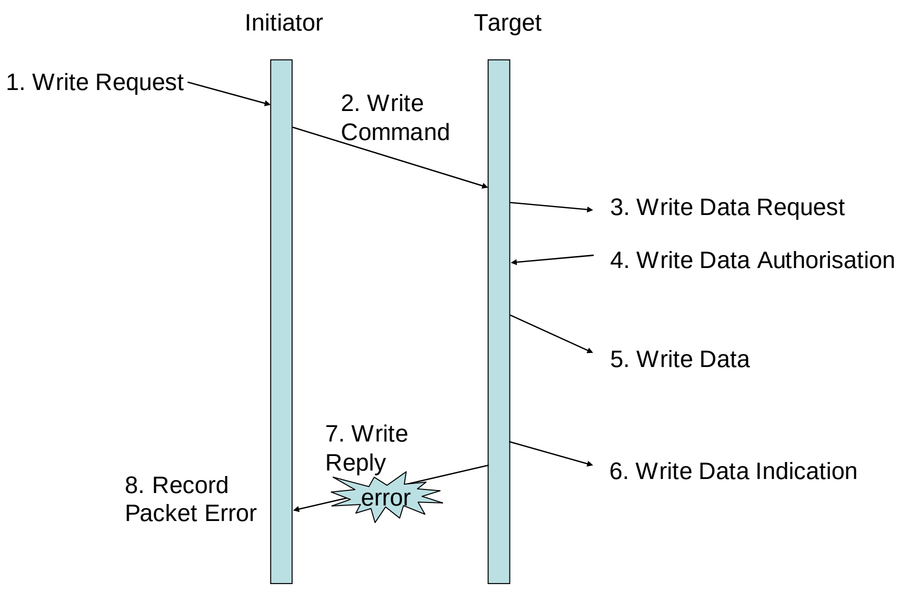


Figure 5-7: Write Reply Error

5.3.3.12 Invalid reply

a. When a reply is received by the initiator (or other node specified by the Reply Address) with the reserved bit in the instruction field set (1) or with the command/reply bit clear (0), the initiator shall discard the reply.

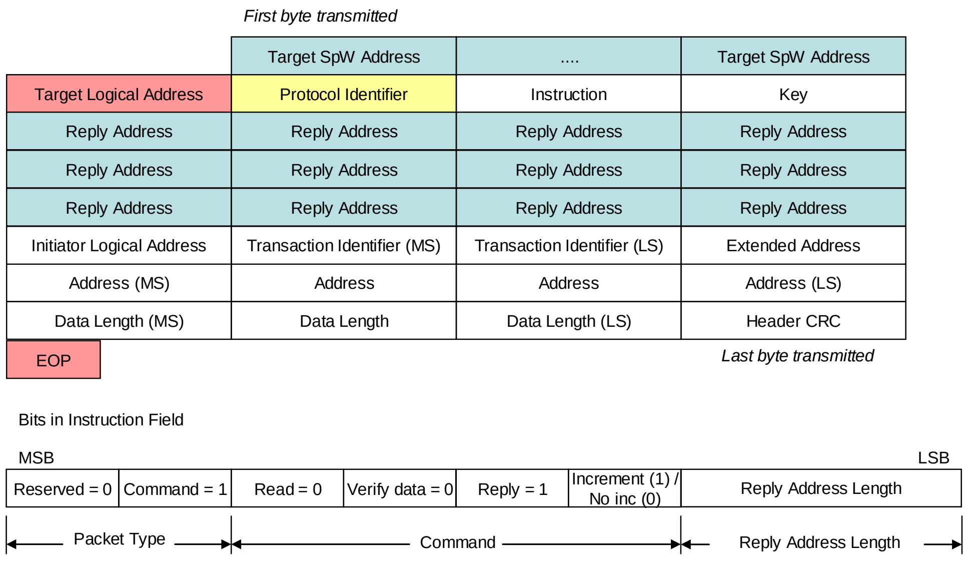
b. When a reply is received by the initiator (or other node specified by the Reply Address) with the reserved bit in the instruction field set (1) or with the command/reply bit clear (0), the initiator should update the error information to reflect the invalid reply error, if the initiator or other node receiving the invalid reply supports error information gathering.

5.4 Read Command

5.4.1 Read command format

5.4.1.1 Fields

a. The read command shall contain the fields shown in Figure 5-8.



First byte transmitted

	Target SpW Address	....	Target SpW Address
Target Logical Address	Protocol Identifier	Instruction	Key
Reply Address	Reply Address	Reply Address	Reply Address
Reply Address	Reply Address	Reply Address	Reply Address
Reply Address	Reply Address	Reply Address	Reply Address
Initiator Logical Address	Transaction Identifier (MS)	Transaction Identifier (LS)	Extended Address
Address (MS)	Address	Address	Address (LS)
Data Length (MS)	Data Length	Data Length (LS)	Header CRC
EOP			

Last byte transmitted

Bits in Instruction Field

MSB LSB

Reserved = 0	Command = 1	Read = 0	Verify data = 0	Reply = 1	Increment (1) / No inc (0)	Reply Address Length

Figure 5-8: Read Command format

5.4.1.2 Target SpaceWire Address field

a. The Target SpaceWire Address field shall be as defined in clause 5.1.1.

5.4.1.3 Target Logical Address field

a. The Target Logical Address field shall be as defined in clause 5.1.2.

5.4.1.4 Protocol Identifier field

a. The Protocol Identifier field shall be as defined in clause 5.1.3.

5.4.1.5 Instruction field

5.4.1.5.1 Instruction field format

a. The Instruction field format shall be as defined in clause 5.1.4.

5.4.1.5.2 Packet type field

a. The Packet Type field shall be 0b01 to indicate that this is a command.

5.4.1.5.3 Command field

a. The Write/Read bit shall be clear (0) for a read command.

b. The Verify-Data-Before-Write bit shall be clear (0) for a read command.

c. The Reply bit shall be set (1) for a read command.

d. The Increment/No increment Address bit shall be:

1. Set (1) if data is read from sequential memory addresses,

2. Clear (0) if data is read from a single memory address.

5.4.1.5.4 Reply Address length field

a. The Reply Address Length field shall be set to the smallest number of 32-bit words that is able to contain the Reply SpaceWire Address from the target, back to the initiator of the command packet or some other node that is to receive the reply.

NOTE For example, if six Reply SpaceWire Address bytes are used then the Reply Address Path Length field is set to two (0b10).

5.4.1.6 Key field

a. The Key field shall be as defined in clause 5.1.5.

5.4.1.7 Reply Address field

a. The Reply Address field shall be as defined in clause 5.1.6.

5.4.1.8 Initiator Logical Address field

a. The Initiator Logical Address field shall be as defined in clause 5.1.7.

5.4.1.9 Transaction Identifier field

a. The Transaction Identifier field format shall be as defined in clause 5.1.8.

5.4.1.10 Extended Address field

a. The Extended Address field shall be as defined in clause 5.1.9.

b. The Extended Address field shall hold the most-significant 8-bits of the starting memory address to be read from.

5.4.1.11 Address field

a. The Address field format shall be as defined in clause 5.1.10.

b. The Address field shall hold the least-significant 32-bits of the starting memory address from which data is read.

5.4.1.12 Data Length field

a. The Data Length field format shall be as defined in clause 5.1.11.

NOTE This gives a maximum Data Length of 16 Megabytes - 1 in a single read command. If a single byte is being read this field is set to one.

If set to zero then no bytes are read from memory which can be used as a test transaction depending upon the implementation.

5.4.1.13 Header CRC

a. The Header CRC field shall contain an 8-bit CRC as defined in clauses 5.1.12 and 5.2.

5.4.1.14 EOP character

a. The end of the Packet containing the read command shall be indicated by an EOP character.

5.4.2 Read reply format

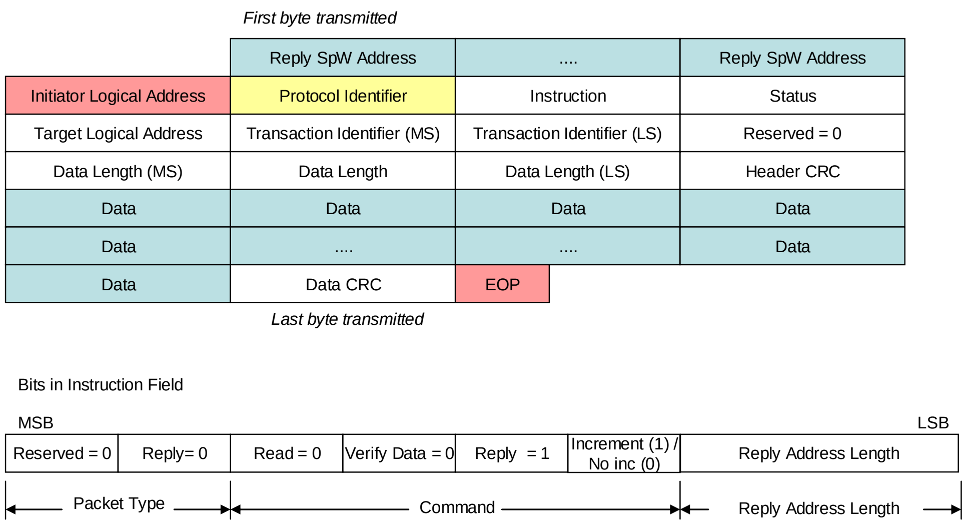
5.4.2.1 General

a. The read reply shall contain either:

1. the data that was read from the target, or
2. an error code indicating why data was not read, or
3. both data and an error code.

5.4.2.2 Format

a. The format of the reply to a read command shall be as in Figure 5-9.



First byte transmitted

	Reply SpW Address	....	Reply SpW Address
Initiator Logical Address	Protocol Identifier	Instruction	Status
Target Logical Address	Transaction Identifier (MS)	Transaction Identifier (LS)	Reserved = 0
Data Length (MS)	Data Length	Data Length (LS)	Header CRC
Data	Data	Data	Data
Data	....	....	Data
Data	Data CRC	EOP	

Last byte transmitted

Bits in Instruction Field

MSB ... LSB

Reserved = 0	Reply= 0	Read = 0	Verify Data = 0	Reply = 1	Increment (1) / No inc (0)	Reply Address Length
Packet Type		Command				Reply Address Length

Figure 5-9: Read Reply format

5.4.2.3 Reply SpW Address

a. The Reply SpaceWire Address field shall comprise zero or more data characters which define how the reply is routed to the initiator or some other node.

b. The SpaceWire address in the Reply SpaceWire Address field shall be constructed from the Reply Address field in the command as detailed in clause 5.1.6.

5.4.2.4 Initiator Logical Address field

a. The Initiator Logical Address field shall be as defined in clause 5.1.7.

5.4.2.5 Protocol Identifier field

a. The Protocol Identifier field shall be as defined in clause 5.1.3.

5.4.2.6 Instruction field

a. The Instruction field format shall be as defined in clause 5.1.4.

b. The Packet Type field shall be 0b00 to indicate that RMAP packet is a reply.

c. The Command field shall be set to the same value as in the Command field of the read command, clause 5.4.1.5.3.

d. The Reply Address Length field shall be set to the same value as in the Reply Address Length field of the read command, clause 5.4.1.5.4.

5.4.2.7 Status field

a. The Status field format shall be as defined in clause 5.1.17.

b. The Status field shall contain:

    1. 0x00 if the command executed successfully,

    2. A non-zero error code if there was an error with the read command as specified in clause 5.6.

5.4.2.8 Target Logical Address field

a. The Target Logical Address field shall be set to either of:

    1. The value of the Target Logical Address field of the read command, see clause 5.3.1.3, or

    2. A logical address of the target.

        NOTE Normally these are the same.

5.4.2.9 Transaction Identifier field

a. The Transaction Identifier field shall be set to the same value as the Transaction Identifier of the read command, see clause 5.4.1.9.

NOTE This is so that the initiator of the read command can associate the reply and data in the reply with the original read command when the reply is sent to the initiator.

5.4.2.10 Data Length field

a. The Data Length field format shall be as defined in clause 5.1.11.

b. The Data Length field in the read reply may have a different value than the data length field in the corresponding read command.

5.4.2.11 Header CRC field

a. The Header CRC field shall contain a CRC as defined in clauses 5.1.12 and 5.2.

5.4.2.12 Data field

a. The Data field shall contain the data that has been read from the memory of the target as defined in clause 5.1.13.

b. The number of data bytes in the reply may be a different value from that indicated in the Data Length field in the command and reply, if fewer bytes are returned than requested.

NOTE If the number of data bytes in the reply is different from the value indicated in the Data Length field the initiator discards the reply as specified in 5.4.3.12.

5.4.2.13 Data CRC field

a. The Data CRC shall contain an 8-bit CRC as defined in clauses 5.1.15 and 5.2.

5.4.2.14 EOP character

a. The end of the Packet containing the read reply shall be indicated by an EOP character.

5.4.3 Read action

5.4.3.1 Overview

The normal sequence of actions for a read command is illustrated in Figure 5-10.

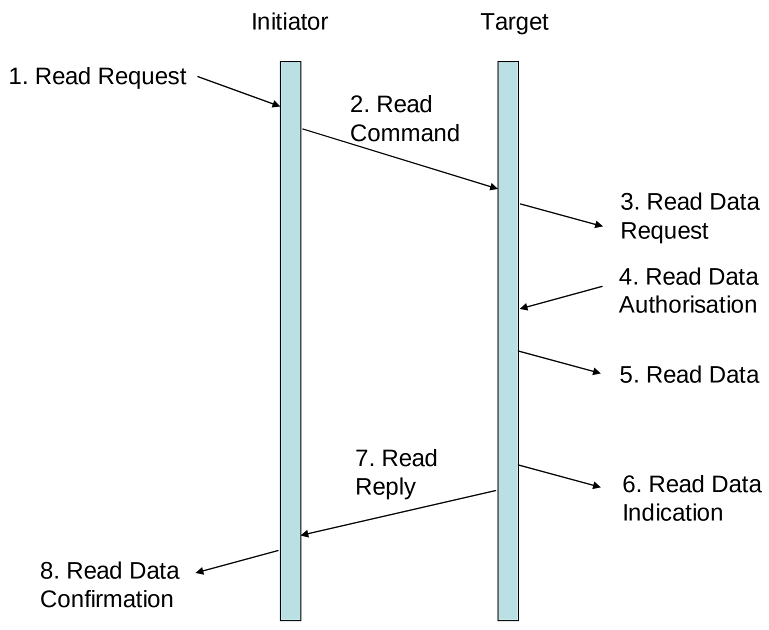


Figure 5-10: Read Command/Reply sequence

5.4.3.2 Read Request

a. The read command sequence shall begin when an initiator user application requests to perform a read operation (Read Request).

b. The initiator user application shall pass the following information to the initiator:

1. Target SpaceWire Address
2. Target Logical Address
3. Read command options
4. Key
5. Reply Address
6. Initiator Logical Address
7. Transaction Identifier
8. Extended Address
9. Memory address
10. Data Length

5.4.3.3 Read command

a. In response to the read request the initiator shall construct the read command including the Header CRC and send it across the SpaceWire network to the target (Read Command).

NOTE The Target SpaceWire Address and Target Logical Address are used to route the command packet to the target.

5.4.3.4 Read data request

5.4.3.4.1 Protocol identifier

a. When a Packet is received at the target and the Protocol Identifier field is 0x01 the packet shall be regarded as an RMAP packet.

5.4.3.4.2 Incomplete header

a. If an EEP or EOP is received before the complete header including header CRC has been received the target shall:

   1. Discard the entire packet,

   2. Not send a reply packet.

b. If an EEP or EOP is received before the complete header including header CRC has been received the target should update the error information to reflect the "EEP" or "Early EOP" error if the target supports error information gathering.

5.4.3.4.3 Error End of Packet

a. If an EEP is received immediately after the complete header including header CRC has been received the target shall:

   1. Discard the entire packet,

   2. Not send a reply packet.

b. If an EEP is received immediately after the complete header including header CRC has been received the target should update the error information to reflect the "EEP" error if the target supports error information gathering.

5.4.3.4.4 Header CRC check

a. When an RMAP packet is received at the target the Header CRC shall be checked.

5.4.3.4.5 Header CRC error

a. When checking the Header CRC indicates an error in the header the target shall:

   1. Discard the entire packet,

   2. Not send a reply packet.

b. When checking the Header CRC indicates an error in the header the target shall update the error information to reflect the "Header CRC" error if the target supports error information gathering.

   NOTE The sequence of events that occurs when there is a CRC error in the header of the read command is illustrated in Figure 5-11.

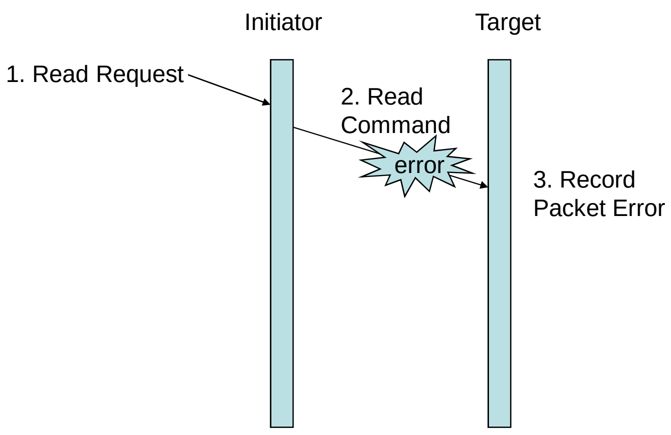


Figure 5-11: Read Command Header Error

5.4.3.4.6 Unused packet type

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11) the target shall:

   1. Discard the command packet,

   2. Not send a reply

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11) the target should update the error information to reflect the "unused RMAP packet type of command code" error if the target supports error information gathering.

c. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11) the target may send a reply containing an "unused RMAP packet type or command code" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

5.4.3.4.7 Invalid command code

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an invalid command code as specified in Table 5-1, the target shall:

   1. Discard the command packet,

   2. Return an "unused RMAP packet type or command code" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields, if a reply has been requested (Reply bit set).

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an invalid command code as specified in Table 5-1, the target should update the error information to reflect the "unused RMAP packet type or command code" error if the target supports error information gathering.

5.4.3.4.8 Data characters in read command

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains a read command (packet type 0b01 and a read command code) and if one or more data characters are received immediately after the complete header including header CRC the target shall:

1. Discard the remainder of the packet,

2. Not execute the read command,

3. Return a "too much data" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains a read command (packet type 0b01 and a read command code) and if one or more data characters are received immediately after the complete header including header CRC the target should update the error information to reflect the "too much data" error if the target supports error information gathering.

5.4.3.4.9 Read data request action

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains a read command (packet type 0b01 and a read command code) the target shall pass the following information to the target user application:

1. Target Logical Address

2. Instruction

3. Key

4. Initiator Logical Address

5. Transaction Identifier

6. Extended Address

7. Memory address

8. Data Length

5.4.3.5 Read data authorisation

5.4.3.5.1 Read operation authorisation

a. The target user application shall be asked to authorise the read operation.

5.4.3.5.2 Invalid key

a. If the value of the Key is not the value expected by the target user application, the target shall:

1. Discard the command packet,

2. Return an "invalid key" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields if a reply has been requested, Reply bit set (1).

b. If the value of the Key is not the value expected by the target user application, the target should update the error information to reflect the "invalid key" error if the target supports error information gathering.

5.4.3.5.3 Invalid logical address

a. If the Target Logical Address is not a logical address recognised by the target user application, the target shall:

1. Discard the command packet,

2. Return an "invalid Target Logical Address" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. If the Target Logical Address is not a logical address recognised by the target user application, the target should update the error information to reflect the "invalid Target Logical Address" error if the target supports error information gathering.

5.4.3.5.4 Command rejection

a. If the command is not accepted by the target user application for any other reason, the target shall:

1. Discard the command packet.

2. Return an "RMAP command not implemented or not authorised" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. If the command is not accepted by the target user application for any other reason, the target should update the error information to reflect the "RMAP command not implemented or not authorised" error if the target supports error information gathering.

NOTE 1 The target user application can reject the command for any reason it likes. For example the address is not 32-bit aligned, the Data Length is not a multiple of 4-bytes, or the address range falls partially or completely outside an acceptable memory address region.

NOTE 2 The sequence of events that occurs when a read command is not authorised is illustrated in Figure 5-12.

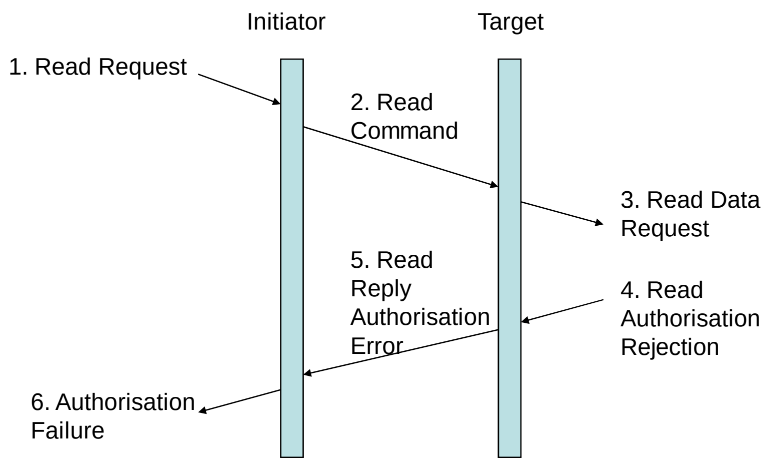


Figure 5-12: Read Authorisation Rejection

5.4.3.6 Read data

a. If authorisation is given by the target user application, data shall be read from the memory location in the target specified by the Extended Address and Address fields (Read Data).

b. If the Increment bit is clear (0) in the command field of the header, the memory address read from in the target shall remain constant i.e. all data is read from the same memory location.

c. If the Increment bit is set (1) in the command field of the header, the memory address read in the target shall be incremented as determined by the target user application in order to access sequential memory locations i.e. the data is read from sequential memory locations.

NOTE The width of the memory locations is determined by the target user application. Byte addressing is not necessarily implied.

5.4.3.7 Read data indication

a. Once data has been read from memory the target user application should be informed that a read operation has taken place (Read Data Indication).

b. If data is not read from memory after authorisation has been given for the read from memory, the target user application shall be informed that an error occurred.

5.4.3.8 Read reply

a. If the read command was executed successfully, the target shall send a reply packet to the node specified by the Reply Address and Initiator Logical Address fields of the read command (Read Reply).

b. The reply to a successful read command shall have:

1. The status field set to 0x00 indicting that there was no error,
2. The Data Length field set to the amount of data read in bytes,
3. The data field filled with the data read from the target memory.

5.4.3.9 Read data confirmation

a. When the read reply is received at the initiator (or other node specified by the Reply Address), successful completion of the read request shall be indicated to the user application on that node (Read Data Confirmation).

b. The Transaction Identifier shall be used to relate the reply to the command that caused the reply.

NOTE It is the responsibility of the initiator user application to read the data in the read reply once it has been informed that the data has been received.

5.4.3.10 Read not OK

a. If the read memory operation memory fails, the target should stop reading from memory as soon as the memory error is detected.

b. If the read memory operation memory fails, the target should update the error information to reflect the memory access error if the target supports error information gathering.

c. If the read memory operation memory fails, the target shall either:

1. Append an EEP to the end of the data already sent in the reply to the initiator, or
2. Append an appropriate data CRC byte covering the data already sent in the reply to the initiator, followed by an EOP.

5.4.3.11 Read reply header error

a. If the reply from the read command arrives at the initiator (or other node specified by the Reply Address) with a Header CRC error, packet type error, or other error in the header, the receiving node shall discard the entire packet containing the corrupted read reply.

b. If the reply from the read command arrives at the initiator (or other node specified by the Reply Address) with a Header CRC error, packet type error, or other error in the header, the receiving node should update the error information to reflect the “Packet Error” error if the initiator (or other node receiving the reply) supports error information gathering.

NOTE The response to an error in the header of a read reply is illustrated in Figure 5-13.

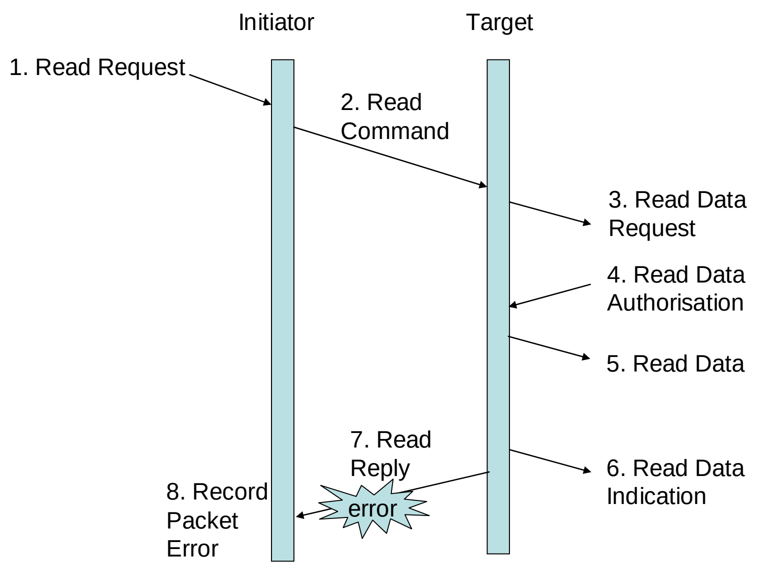


Figure 5-13: Read Reply Header Error

5.4.3.12 Read reply data error

a. If the header of the read reply packet is received intact by the initiator (or other node specified by the Reply Address) but the data field is corrupted as indicated by an incorrect data field length (too long or too short) or by a Data CRC error, the initiator shall discard the reply.

b. If the header of the read reply packet is received intact by the initiator (or other node specified by the Reply Address) but the data field is corrupted as indicated by an incorrect data field length (too long or too short) or by a Data CRC error, the initiator should:

   1. Update the error information to reflect the “invalid reply” error, if the initiator or other node receiving the invalid reply supports error information gathering,

   2. Indicate an error to the user application in the node receiving the reply (Read Data Failure).

      NOTE The response to an error in the data field of a read reply is illustrated in Figure 5-14.

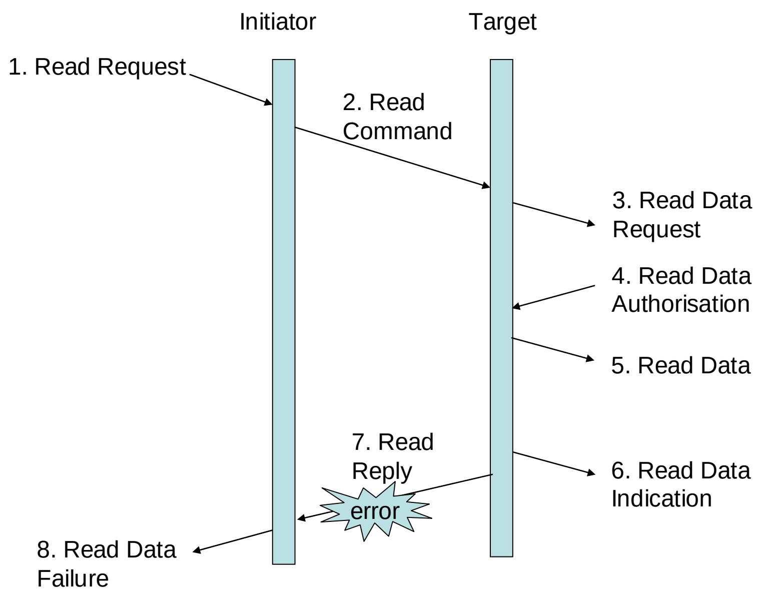


Figure 5-14: Read Reply Data Error

5.4.3.13 Invalid reply

a. When a reply is received by the initiator (or other node specified by the Reply Address) with the reserved bit in the instruction field set (1) or with the command/reply bit clear (0), the initiator shall discard the reply.

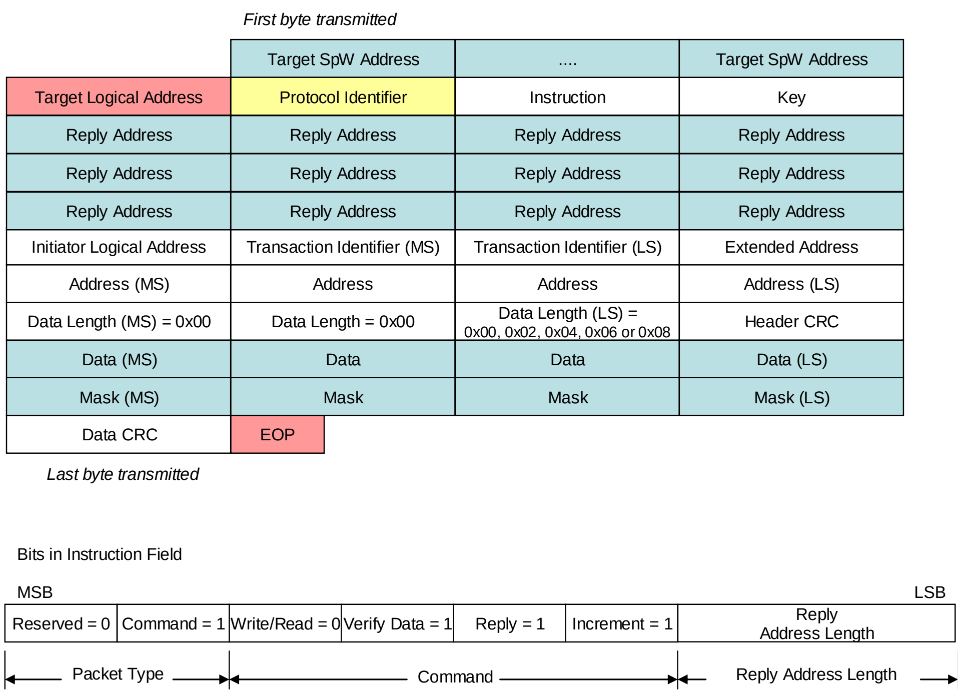
b. When a reply is received by the initiator (or other node specified by the Reply Address) with the reserved bit in the instruction field set (1) or with the command/reply bit clear (0), the initiator should update the error information to reflect the "invalid reply" error, if the initiator or other node receiving the invalid reply supports error information gathering.

5.5 Read-Modify-Write Command

5.5.1 Read-modify-write command format

5.5.1.1 Fields

a. The read-modify-write command shall contain the fields shown in Figure 5-15.



First byte transmitted

	Target SpW Address	....	Target SpW Address
Target Logical Address	Protocol Identifier	Instruction	Key
Reply Address	Reply Address	Reply Address	Reply Address
Reply Address	Reply Address	Reply Address	Reply Address
Reply Address	Reply Address	Reply Address	Reply Address
Initiator Logical Address	Transaction Identifier (MS)	Transaction Identifier (LS)	Extended Address
Address (MS)	Address	Address	Address (LS)
Data Length (MS) = 0x00	Data Length = 0x00	Data Length (LS) = 0x00, 0x02, 0x04, 0x06 or 0x08	Header CRC
Data (MS)	Data	Data	Data (LS)
Mask (MS)	Mask	Mask	Mask (LS)
Data CRC	EOP		

Last byte transmitted

Bits in Instruction Field

MSB LSB

Reserved = 0	Command = 1	Write/Read = 0	Verify Data = 1	Reply = 1	Increment = 1	Reply Address Length

Figure 5-15: Read-Modify-Write Command format

5.5.1.2 Target SpaceWire Address field

a. The Target SpaceWire Address field shall be as defined in clause 5.1.1.

5.5.1.3 Target Logical Address field

a. The Target Logical Address field shall be as defined in clause 5.1.2.

5.5.1.4 Protocol Identifier field

a. The Protocol Identifier field shall be as defined in clause 5.1.3.

5.5.1.5 Instruction field

5.5.1.5.1 Instruction field format

a. The Instruction field format shall be as defined in clause 5.1.4.

5.5.1.5.2 Packet type field

a. The Packet Type field shall be 0b01 to indicate that this is a command.

5.5.1.5.3 Command field

a. The Write/Read bit shall be clear (0) for a read-modify-write command.

b. The Verify-Data-Before-Write bit shall be set (1) for a read-modify-write command.

NOTE This is so that the data is verified before it is written to memory and also distinguishes a read-modify-write from a read command.

c. The Reply bit shall be set (1) for a read-modify-write command.

NOTE The reply contains the data initially read from the memory in the target.

d. The "Increment / No Increment Address" bit shall be set (1) for a read-modify-write command.

NOTE This means that when read-modify-write is applied to more than one byte, the address is incremented if byte wide memory is being used. Note that the width of the memory word is determined by the target unit and can be any multiple of 8-bits.

5.5.1.5.4 Reply Address length field

a. The Reply Address Length field shall be set to the smallest number of 32-bit words that is able to contain the Reply SpaceWire Address from the target, back to the initiator of the command packet or some other node that is to receive the reply.

NOTE For example, if ten path Reply SpaceWire Address bytes are used then the Reply Address Length field is set to three (0b11).

5.5.1.6 Key field

a. The Key field shall be as defined in clause 5.1.5.

5.5.1.7 Reply Address field

a. The Reply Address field shall be as defined in clause 5.1.6.

5.5.1.8 Initiator Logical Address field

a. The Initiator Logical Address field shall be as defined in clause 5.1.7.

5.5.1.9 Transaction Identifier field

a. The Transaction Identifier field format shall be as defined in clause 5.1.8.

5.5.1.10 Extended Address field

a. The Extended Address field shall be as defined in clause 5.1.9.

b. The Extended Address field shall hold the most-significant 8-bits of the starting memory address to be read from.

5.5.1.11 Address field

a. The Address field format shall be as defined in clause 5.1.10.

b. The Address field shall hold the least-significant 32-bits of the memory address to which the data in a read-modify-write command is read from and written to.

5.5.1.12 Data Length field

a. The Data Length field format shall be as defined in clause 5.1.11.

b. The Data Length field shall contain the overall length, in bytes, of the data and mask fields i.e. the length of the data field plus the length of the mask field.

c. In a read-modify-write command the Data Length shall specify the size of the data field plus the size of the mask field sent in the command, which is twice the amount of data read and written.

NOTE For example, if a 2-byte word is written, then the Data Length is 0x04. There are two data bytes and two mask bytes in the command. Two bytes are read from memory and returned to the initiator. Two bytes are written combining the read data, the data from the command and the mask.

d. The Data Length shall only take on values of 0x00, 0x02, 0x04, 0x06 or 0x08, which correspond to the reading, modifying and writing of 0, 1, 2, 3, or 4 bytes of data respectively.

5.5.1.13 Header CRC field

a. The Header CRC field shall contain an 8-bit CRC as defined in clauses 5.1.12 and 5.2.

5.5.1.14 Data field

a. The Data field shall contain the data that is combined with the mask and the data read from memory before the result is written into the memory of the target as defined in clause 5.1.13.

b. The set of 0, 1, 2, 3 or 4 data bytes shall precede the corresponding set of 0, 1, 2, 3, or 4 mask bytes.

5.5.1.15 Mask field

a. The Mask field shall be used by the target application to define how the data written to memory is formed.

NOTE 1 The way the read data and mask are combined is application dependent.

NOTE 2 For example, data written can be selected on a bit by bit basis from the data sent in the command when the corresponding mask bit is set (1) or from the data read in the reply when the mask bit is clear (0).

NOTE 3 Written Data = (Mask AND Command_Data) OR (NOT Mask AND Read_Data).

NOTE 4 This example is illustrated in Figure 5-16. The target user application can implement different schemes for example test and set.

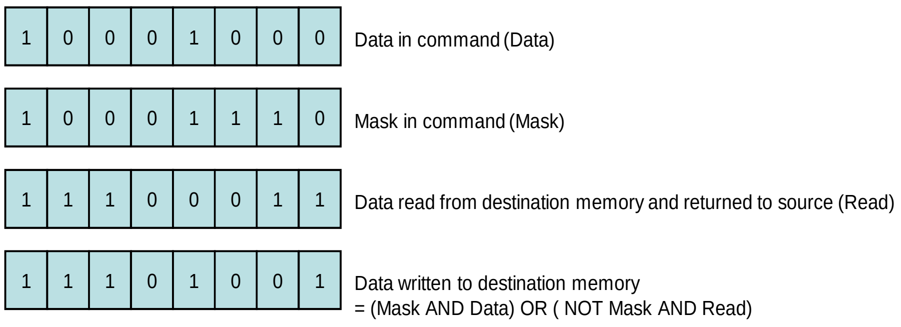

1 0 0 0 1 0 0 0
Data in command (Data)
1 0 0 0 1 1 1 0
Mask in command (Mask)
1 1 1 0 0 0 1 1
Data read from destination memory and returned to source (Read)
1 1 1 0 1 0 0 1
Data written to destination memory
= (Mask AND Data) OR (NOT Mask AND Read)

Figure 5-16: Example Operation of Read-Modify-Write Command

5.5.1.16 Data CRC field

a. The Data CRC shall contain an 8-bit CRC as defined in clauses 5.1.15 and 5.2.

b. The Data CRC shall cover both the data and the mask fields.

5.5.1.17 EOP character

a. The end of the Packet containing the read-modify-write command shall be indicated by an EOP character.

5.5.2 Read-modify-write reply format

5.5.2.1 General

a. The read-modify-write reply shall contain either:

   1. the data that was read from the target, or

   2. an error code indicating why data was not read, or

   3. both data and an error code.

5.5.2.2 Format

a. The format of the reply to a read-modify-write command shall be as in Figure 5-17.

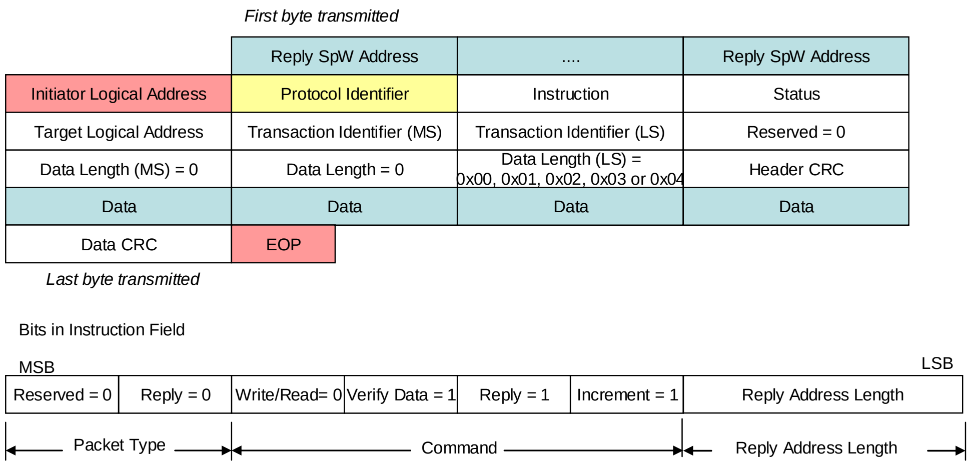


Figure 5-17: Read-Modify-Write Reply format

5.5.2.3 Reply SpaceWire Address

a. The Reply SpaceWire Address field shall comprise zero or more data characters which define how the reply is routed to the initiator or some other node.

b. The SpaceWire address in the Reply SpaceWire Address field shall be constructed from the Reply Address field in the command as detailed in clause 5.1.6.

5.5.2.4 Initiator Logical Address field

a. The Initiator Logical Address field shall be as defined in clause 5.1.7.

5.5.2.5 Protocol Identifier field

a. The Protocol Identifier field shall be as defined in clause 5.1.3.

5.5.2.6 Instruction field

a. The Instruction field format shall be as defined in clause 5.1.4.

b. The Packet Type field shall be 0b00 to indicate that RMAP packet is a reply.

c. The Command field shall be set to the same value as in the Command field of the read-modify-write command, 5.5.1.5.3.

d. The Reply Address Length field shall be set to the same value as in the Reply Address Length field of the read-modify-write command, clause 5.5.1.5.3.

5.5.2.7 Status field

a. The Status field format shall be as defined in clause 5.1.17.

b. The Status field shall contain:

1. 0x00 if the command executed successfully,

2. A non-zero error code if there was an error with the read-modify-write command as specified in clause 5.6.

5.5.2.8 Target Logical Address field

a. The Target Logical Address field shall be set to either of:

1. The value of the Target Logical Address field of the write command, see clause 5.3.1.3, or
2. A logical address of the target.

NOTE Normally these are the same.

5.5.2.9 Transaction Identifier field

a. The Transaction Identifier field shall be set to the same value as the Transaction Identifier of the read-modify-write command, see clause 5.5.1.9.

NOTE This is so that the initiator of the read-modify-write command can associate the reply and data in the reply with the original read-modify-write command.

5.5.2.10 Data Length field

a. The Data Length field format shall be as defined in clause 5.1.11.

b. The Data Length field shall contain the length, in bytes, of the data returned in the reply packet.

c. For a read-modify-write command the Data Length shall be 0, 1, 2, 3 or 4 only.

NOTE The Data Length in the reply is a different value to the Data Length in the command since the Data Length in the command includes both data and mask.

5.5.2.11 Header CRC field

a. The Header CRC field shall contain a CRC as defined in clauses 5.1.12 and 5.2.

5.5.2.12 Data field

a. The Data field shall contain the data that has been read from the memory of the target as defined in clause 5.1.13.

NOTE The data length field in the reply is different to that in the command.

5.5.2.13 Data CRC field

a. The Data CRC shall contain an 8-bit CRC as defined in clauses 5.1.15 and 5.2.

5.5.2.14 EOP character

a. The end of the Packet containing the read-modify-write reply shall be indicated by an EOP character.

5.5.3 Read-modify-write action

5.5.3.1 Overview

The normal sequence of actions for a read-modify-write command is illustrated in Figure 5-18.

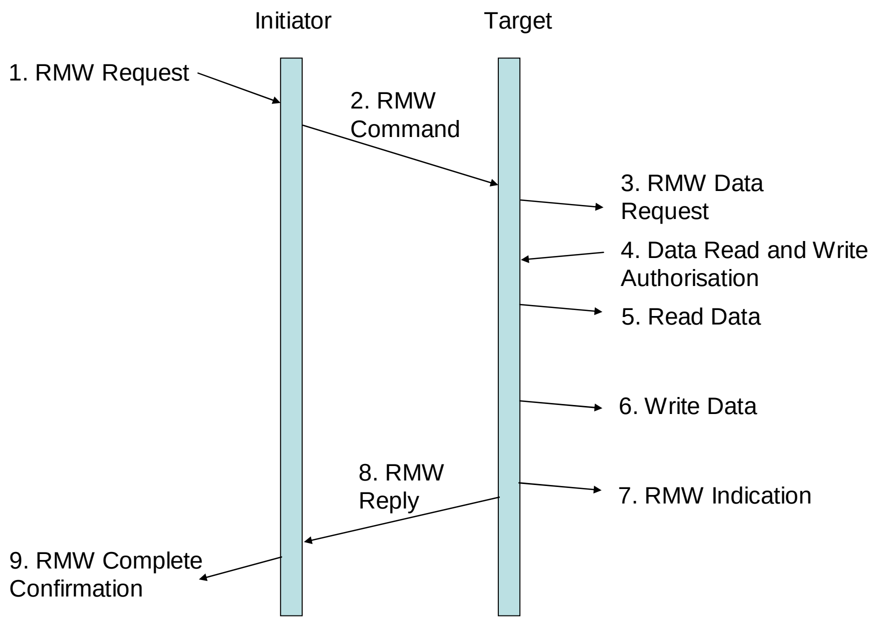


Figure 5-18: Read-Modify-Write Command/Reply sequence

5.5.3.2 Read-modify-write request

a. The read-modify-write command sequence shall begin when an initiator user application requests to perform a read-modify-write operation (RMW Request).

b. The initiator user application shall pass the following information to the initiator:

1. Target SpaceWire Address
2. Target Logical Address
3. Read-modify-write command options
4. Key
5. Reply Address
6. Initiator Logical Address
7. Transaction Identifier

8. Extended Address
9. Memory address
10. Data Length
11. Data
12. Mask

5.5.3.3 Read-modify-write command

a. In response to the read-modify-write request the initiator shall construct the read-modify-write command including the Header CRC and Data CRC and send it across the SpaceWire network to the target (RMW Command).

NOTE The Target SpaceWire Address and Target Logical Address are used to route the command packet to the target.

5.5.3.4 Read-modify-write data request

5.5.3.4.1 Protocol identifier

a. When a Packet is received at the target and the Protocol Identifier field is 0x01 the packet shall be regarded as an RMAP packet.

5.5.3.4.2 Incomplete header

a. If an EEP or EOP is received before the complete header including header CRC has been received the target shall:

1. Discard the entire packet,
2. Not send a reply packet.

b. If an EEP or EOP is received before the complete header including header CRC has been received the target should update the error information to reflect the an "EEP" or "Early EOP" error if the target supports error information gathering.

5.5.3.4.3 Error End of Packet

a. If an EEP is received immediately after the complete header including header CRC has been received the target shall discard the entire packet.

b. If an EEP is received immediately after the complete header including header CRC has been received the target should:

1. Update the error information to reflect the "EEP" error if the target supports error information gathering,
2. Not send a reply packet.

5.5.3.4.4 Header CRC check

a. When an RMAP packet is received at the target the Header CRC shall be checked.

5.5.3.4.5 Header CRC error

a. When the Header CRC indicates an error in the header the target shall:

1. Discard the entire packet,
2. Not send a reply packet.

b. When the Header CRC indicates an error in the header the target should update the error information to reflect the "Header CRC" error if the target supports error information gathering.

NOTE The sequence of events that occurs when there is a CRC error in the header of the read-modify-write command is illustrated in Figure 5-19.

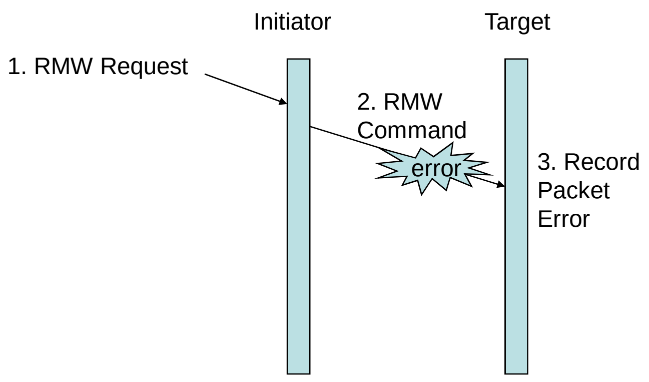


Figure 5-19: Read-Modify-Write Command Header Error

5.5.3.4.6 Unused packet type

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11), the target shall:

1. Discard the command packet,
2. Not send a reply.

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11), the target should update the error information to reflect the "unused RMAP packet type of command code" error if the target supports error information gathering.

c. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an unused packet type (0b10 or 0b11), the target may send a reply containing an "unused RMAP packet type or command code" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

5.5.3.4.7 Invalid command code

a. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an invalid command code as specified in Table 5-1 the target shall:

1. Discard the command packet,
2. Return an "unused RMAP packet type or command code" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if the Instruction field contains an invalid command code as specified in Table 5-1, the target should update the error information to reflect the "unused RMAP packet type or command code" error if the target supports error information gathering.

5.5.3.4.8 Data CRC check

a. When checking the Header CRC indicates no error present in the header, the data and mask shall be buffered and checked using the Data CRC before command authorisation is requested.

5.5.3.4.9 Read-modify-write data action

a. When checking the Header CRC indicates no error present in the header and if the data exceeds the available buffer space, the target shall:

1. Neither read data from, nor write data to memory,
2. Return a "verify buffer overrun" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if the data exceeds the available buffer space, the target should update the error information to reflect the "verify buffer overrun" error if the target supports error information gathering.

5.5.3.4.10 Data CRC error

a. When checking the Header CRC indicates no error present in the header and if the Data CRC is in error the target shall:

1. Neither read data from, nor write data to memory,
2. Return an "invalid Data CRC" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if the Data CRC is in error the target should update the error information to reflect the "invalid Data CRC" error if the target supports error information gathering.

5.5.3.4.11 Unexpected EOP

a. When checking the Header CRC indicates no error present in the header and if there is less data in the data field than specified in the Data Length field of the read-modify-write command header when the EOP is reached, the target shall:

1. Neither read data from, nor write data to memory,

2. Return an "early EOP" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if there is less data in the data field than specified in the Data Length field of the read-modify-write command header when the EOP is reached, the target should:

   1. Indicate that an "insufficient data" error has occurred to the user application in the target,

   2. Update the error information to reflect the "insufficient data" error if the target supports error information gathering.

5.5.3.4.12 More data than expected

a. When checking the Header CRC indicates no error present in the header and if there is more data in the data field than specified in the Data Length field of the read-modify-write command header, then the target shall:

   1. Neither read data from, nor write data to memory,

   2. Discard the rest of the packet,

   3. Return a "too much data" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if there is more data in the data field than specified in the Data Length field of the read-modify-write command header, then the target should update the error information to reflect "too much data" error if the target supports error information gathering.

5.5.3.4.13 Invalid Data Length field

a. When checking the Header CRC indicates no error present in the header and if the value in the Data Length field is incorrect (i.e. is not 0, 2 ,4, 6 or 8), the target shall:

   1. Neither read data from, nor write data to memory,

   2. Return a "read-modify-write Data Length" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if the value in the Data Length field is incorrect (i.e. is not 0, 2 ,4, 6 or 8), the target should update the error information to reflect "read-modify-write Data Length" error if the target supports error information gathering.

   NOTE The sequence of events that occurs when there is an error in the data field of the read-modify-write command is illustrated in Figure 5-20.

5.5.3.4.14 Error End of Packet

a. When checking the Header CRC indicates no error present in the header and if the packet ends in an EEP, the target shall:

1. Not write data into memory,
2. Return an "EEP" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. When checking the Header CRC indicates no error present in the header and if the packet ends in an EEP, the target should:

1. Indicate that an "EEP" error has occurred to the user application in the target,
2. Update the error information to reflect the "EEP" error if the target supports error information gathering.

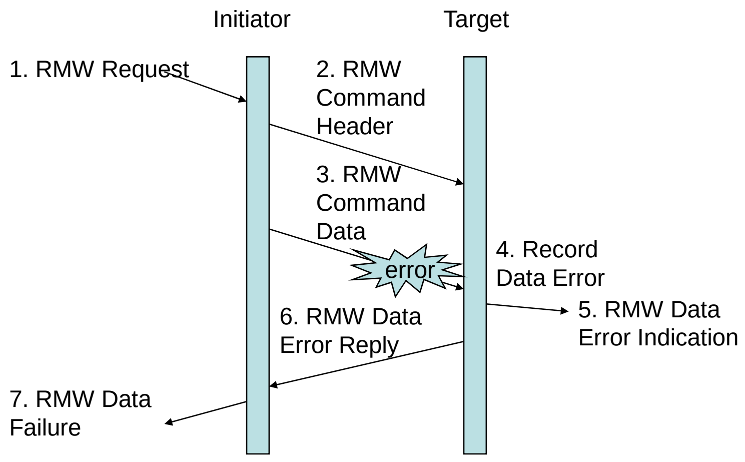


Figure 5-20: Read-Modify-Write Command Data Error

5.5.3.4.15 Read-modify-write data request action

a. When checking the Header CRC indicates no error present in the header and if the instruction field contains a RMW command and the Data CRC is correct and the amount of data in the data field is correct, the target shall pass the following information to the target user application for command authorisation:

1. Target Logical Address
2. Instruction
3. Key
4. Initiator Logical Address
5. Transaction Identifier
6. Extended Address
7. Memory address
8. Data Length

5.5.3.5 Read-modify-write authorisation

5.5.3.5.1 Read-modify-write operation authorisation

a. The target user application shall be asked to authorise the read-modify-write operation.

5.5.3.5.2 Invalid key

a. If the value of the Key is not the value expected by the target user application, the target shall:

   1. Discard the command packet,

   2. Return an "invalid key" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. If the value of the Key is not the value expected by the target user application, the target should update the error information to reflect the "invalid key" error if the target supports error information gathering.

5.5.3.5.3 Invalid logical address

a. If the Target Logical Address is not a logical address recognised by the target user application, the target shall

   1. Discard the command packet,

   2. Return an "invalid Target Logical Address" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. If the Target Logical Address is not a logical address recognised by the target user application, the target should update the error information to reflect the "invalid Target Logical Address" error if the target supports error information gathering.

5.5.3.5.4 Command rejection

a. If the command is not accepted by the target user application for any other reason, the target shall:

   1. Discard the command packet,

   2. Return an "RMAP command not implemented or not authorised" error as specified in clause 5.6 to the node specified in the Reply Address and Initiator Logical Address fields.

b. If the command is not accepted by the target user application for any other reason, the target should update the error information to reflect the "RMAP command not implemented or not authorised" error if the target supports error information gathering.

NOTE 1 The target user application can reject the command for any reason it likes.

NOTE 2 For example, if the read operation is acceptable, but the write operation is not acceptable, due to for instance a write protected memory, then the command is not authorised by the target user application when the read-modify-write data request is made.

NOTE 3 The sequence of events that occurs when a read-modify-write command is not authorised is illustrated in Figure 5-21.

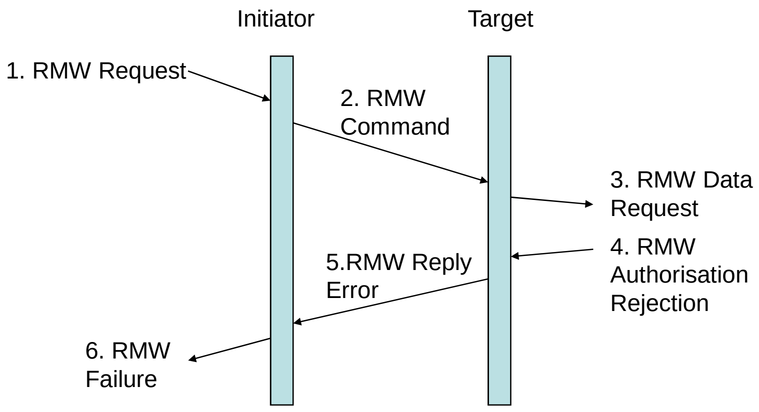


Figure 5-21: Read-Modify-Write Authorisation Rejection

5.5.3.6 Read data

a. If the data to be written does not contain any errors and authorisation is given by the target user application, data shall be read from the memory location in the target specified by the Extended Address and Address fields (Read Data).

b. The memory address read from in the target shall be incremented as determined by the target user application in order to access sequential memory locations i.e. the data is read from sequential memory locations.

NOTE The width of the memory locations is determined by the target user application. Byte addressing is not necessarily implied.

5.5.3.7 Write data

a. The data to be written to the memory locations shall be calculated from the data read from memory and the data and mask fields of the read-modify-write command.

NOTE The way in which the data read from target memory is combined with the data and mask values in the command is application dependent.

b. The new data shall be written to the memory location(s) that was previously read.

5.5.3.8 Read-modify write data indication

a. Once data has been read and written to memory the user application running on the target should be informed that a read-modify-write operation has taken place (RMW Indication).

b. If data is not written to memory after authorisation has been given for the read-modify-write to memory, the target user application should be informed that an error occurred.

5.5.3.9 Read-modify-write reply

a. If the read-modify-write command was executed successfully, the target shall send a reply packet to the node specified by the Reply Address and Initiator Logical Address fields of the read command (RMW Reply).

b. The reply to a successful read-modify-write command shall have:

   1. The status field set to 0x00 indicting that there was no error,

   2. The Data Length field set to the amount of data read in bytes,

   3. The data field filled with the data read from the target memory.

5.5.3.10 Read-modify-write complete confirmation

a. When the read-modify-write reply is received at the initiator (or other node specified by the Reply Address), successful completion of the read-modify-write request shall be indicated to the user application on that node (RMW Complete Confirmation).

b. The Transaction Identifier shall be used to relate the reply to the command that caused the reply.

   NOTE It is the responsibility of the initiator user application to read the data in the read reply once it has been informed that the data has been received.

5.5.3.11 Read and Write not OK

a. If the read or write operations to memory fails, the target should stop reading to or writing from memory as soon as the memory error is detected.

b. If the read or write operations to memory fails, the target should update the error information to reflect the "memory access" error if the target supports error information gathering.

c. If the read or write operations to memory fails, the target shall either:

   1. Append an EEP to the end of the data sent in the reply to the initiator, or

   2. Append an appropriate data CRC byte covering the data sent in the reply to the initiator, followed by an EOP.

      NOTE In this case the data length field in the reply contains the amount of data requested which is different to the amount of data returned in the data field of the reply.

5.5.3.12 Read-modify-write reply header error

a. If the reply from the read-modify-write command arrives at the initiator (or other node) with a Header CRC error, packet type error, or other error in the header, the receiving node shall discard the entire packet containing the corrupted read-modify-write reply.

b. If the reply from the read-modify-write command arrives at the initiator (or other node) with a Header CRC error, packet type error, or other error in the header, the receiving node should update the error information to reflect the "Packet Error" error if the initiator (or other node receiving the reply) supports error information gathering.

NOTE The response to an error in the header of a read-modify-write reply is illustrated in Figure 5-22. The data has been correctly read from target memory, modified using the mask information and the result written back into memory. The target application has been informed. The read-modify-write reply that is sent back to the initiator is corrupted.

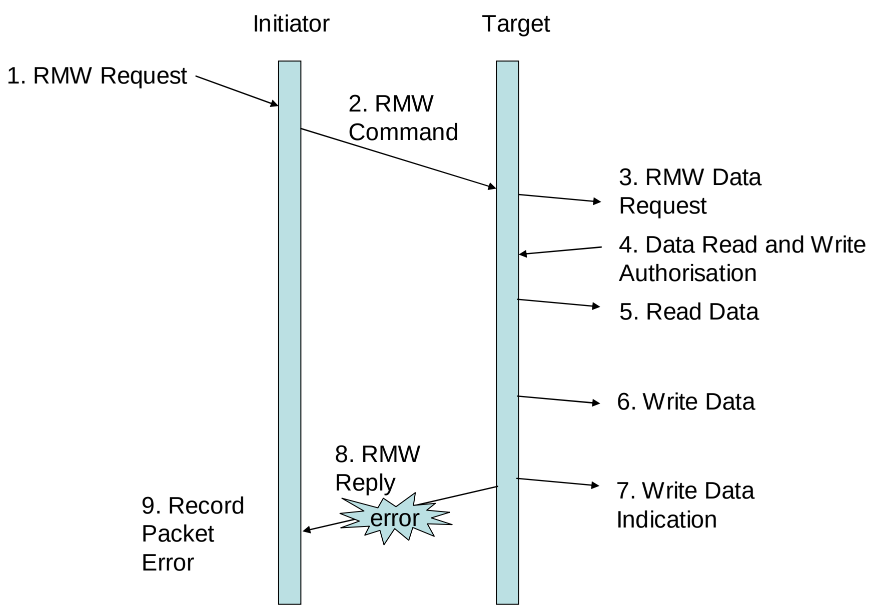


Figure 5-22: Read-Modify-Write Reply Error

5.5.3.13 Read-modify-write reply data error

a. If the header of the read-modify-write reply packet is received intact by the initiator (or other node specified by the Reply Address) but the data field is corrupted as indicated by an incorrect data field length (too long or too short) or by a Data CRC error, the initiator shall discard the reply.

b. If the header of the read-modify-write reply packet is received intact by the initiator (or other node specified by the Reply Address) but the data field is corrupted as indicated by an incorrect data field length (too long or too short) or by a Data CRC error, the initiator should:

1. Update the error information to reflect the "invalid reply" error, if the initiator or other node receiving the invalid reply supports error information gathering.

2. Indicate an error to the user application in the node receiving the reply (Read-Modify-Write Data Failure).

   NOTE The response to an error in the data field of a read reply is illustrated in Figure 5-23.

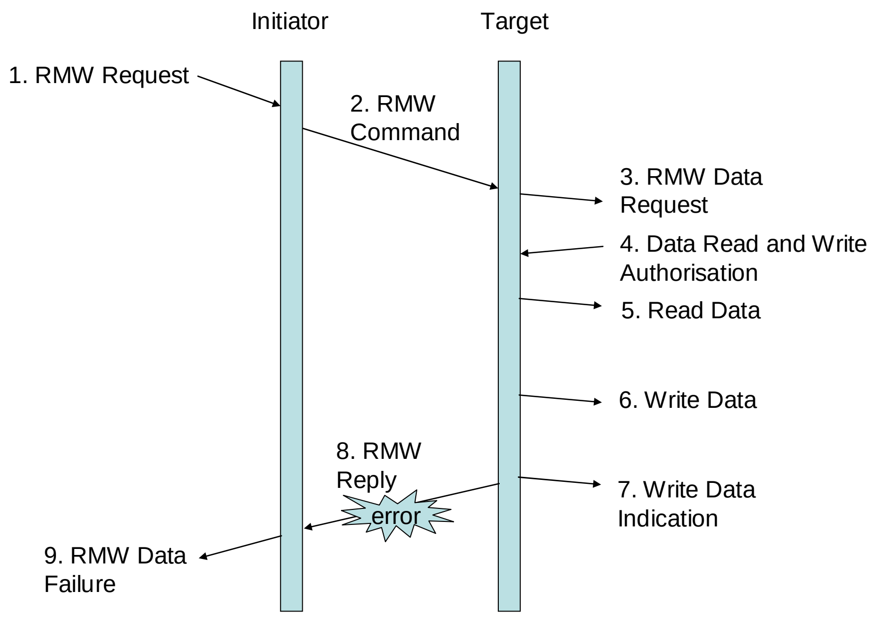


Figure 5-23: RMW Reply Data Error

5.5.3.14 Invalid reply

a. When a reply is received by the initiator (or other node specified by the Reply Address) with the reserved bit in the instruction field set (1) or with the command/reply bit clear (0), the initiator shall discard the reply.

b. When a reply is received by the initiator (or other node specified by the Reply Address) with the reserved bit in the instruction field set (1) or with the command/reply bit clear (0), the initiator should update the error information to reflect the "invalid reply" error, if the initiator or other node receiving the invalid reply supports error information gathering.

5.6 Error and status codes

5.6.1 Error and status codes

a. The set of error and status codes that shall be used are listed in Table 5-4.

b. If a command executes successfully, then the Error Code 0 shall be used in the Status field of any reply.

c. If there is an error with the command, then a suitable error code as defined in Table 5-4 shall be used in the Status field of any reply.

d. When one or more errors arise that mean more than one error code is applicable it shall be application dependent as to which of the relevant error codes is sent.

e. The specific error returned may vary depending on the specific application.

Table 5-4: Error and Status codes

Error code	Error	Error description	Applicability		
			Write	Read	RMW
0	Command executed successfully		X	X	X
1	General error code	The detected error does not fit into the other error cases or the node does not support further distinction between the errors	X	X	X
2	Unused RMAP Packet Type or Command Code	The Header CRC was decoded correctly but the packet type is reserved or the command is not used by the RMAP protocol.	X	X	X
3	Invalid key	The Header CRC was decoded correctly but the device key did not match that expected by the target user application	X	X	X
4	Invalid Data CRC	Error in the CRC of the data field	X		X
5	Early EOP	EOP marker detected before the end of the data	X	X	X
6	Too much data	More than the expected amount of data in a command has been received	X	X	X
7	EEP	EEP marker detected immediately after the header CRC or during the transfer of data and Data CRC or immediately thereafter. Indicates that there was a communication failure of some sort on the network	X	X	X
8	Reserved	Reserved			
9	Verify buffer overrun	The verify before write bit of the command was set so that the data field was buffered in order to verify the Data CRC before transferring the data to target memory. The data field was longer than able to fit inside the verify buffer resulting in a buffer overrun Note that the command is not executed in this case	X		X
10	RMAP Command not implemented or not authorised	The target user application did not authorise the requested operation. This may be because the command requested has not been implemented	X	X	X
11	RMW Data Length error	The amount of data in a RMW command is invalid (0x01, 0x03, 0x05, 0x07 or greater than 0x08)			X
12	Invalid Target Logical Address	The Header CRC was decoded correctly but the Target Logical Address was not the value expected by the target	X	X	X
13-255	Reserved	All unused error codes are reserved			

5.7 Partial Implementations of RMAP

5.7.1 Limited functionality nodes

5.7.1.1 Types of RMAP node

a. The functionality shall be provided to implement nodes which are:

1. Initiators,
2. Targets, or
3. Both initiators and targets.

5.7.1.2 Initiator only node

a. An initiator shall be able to send RMAP commands and receive RMAP replies.

b. If an initiator only node receives a command, the command shall be discarded.

c. The "Command Received by Initiator" error should be recorded.

5.7.1.3 Target only node

a. A target shall be able to receive RMAP commands and send replies.

b. If a target only node receives a reply, the reply shall be discarded.

c. The "Reply Received by Target" error should be recorded.

5.7.2 Partial implementations

a. Partial implementations of RMAP may be permitted where only some commands or command options are supported.

NOTE For example, a unit that supports write and read command but does not implement the read-modify-write command.

b. If the target user application is passed a command or a command with options that it does not support then it shall not authorise the command.

c. If a reply has been requested then the RMAP command not implemented or not authorised error shall be sent back to the initiator (or other node).

NOTE See clause 5.6.

5.8 RMAP conformance

5.8.1 Overview

Several SpaceWire RMAP compatible subsets can be identified each of which implements only a part of the SpaceWire RMAP standard:

1. RMAP Write Command / Target only
2. RMAP Read Command / Target only
3. RMAP Read-Modify-Write Command / Target only
4. RMAP Read and Write / Target only
5. RMAP Initiator
6. RMAP Initiator and Target

Corresponding subsets of the SpaceWire RMAP standard are defined to which implementations can claim conformance. The form of the conformance statement to use is the one given by the appropriate subset definition in the following clauses.

An RMAP compliant product can implement one or more of these subsets.

5.8.2 RMAP partial implementations

5.8.2.1 Target only

a. An implementation of "SpaceWire RMAP Target only" shall conform to all requirements given in clause 5.7.1.3 and may claim such compliance by using the following conformance statement *"This product conforms to the SpaceWire RMAP Target only specification of the ECSS SpaceWire Protocols Standard (ECSS-E-ST-50-52)"*.

5.8.2.2 Initiator only

a. An implementation of "SpaceWire Initiator only" shall conform to all requirements given in 5.7.1.2 and may claim such compliance by using the following conformance statement *"This product conforms to the SpaceWire RMAP Initiator only specification of the ECSS SpaceWire Protocols Standard ECSS-E-ST-50-52)"*.

5.8.2.3 RMAP Write Command

a. An implementation of "SpaceWire RMAP Write Command" shall conform to all requirements given in Table 5-5 and may claim such compliance by using the following conformance statement *"This product conforms to the SpaceWire RMAP Write specification of the ECSS SpaceWire Protocols Standard (ECSS-E-ST-50-52)"*.

b. The supplier of the RMAP equipment shall provide a table detailing the write characteristics of the RMAP implementation.

NOTE An example of the required table is given in Table 5-6.

Table 5-5: SpaceWire RMAP write command

Relevant clauses	Title
ECSS-E-ST-50-51, Clause 5	Protocol Identifier
5.3	Write Command
5.6	Error Codes

Table 5-6: Example of Write Command Product Characteristics

Write Command			
Action	**Supported**	**Maximum Data Length (bytes)**	**Non-aligned access accepted**
8-bit write	No	-	-
16-bit write	No	-	-
32-bit write	Yes	12	No
64-bit write	No	-	-
Verified write	Yes	4	No
Word or byte address	Word address 32-bit aligned		
Endian order	Little endian i.e. first byte received goes in least significant byte of memory location		
Accepted logical addresses	0xFE at power-on 0x42-0x51 after initialisation		
Target logical address in reply	What was in command		
Accepted keys	0x20		
Accepted address ranges	0x00 0000 0000 - 0x00 0000 001C		
Address incrementation	Incrementing address only		
Status codes returned	All		

5.8.2.4 RMAP Read Command

a. An implementation of the "SpaceWire RMAP Read Command" shall conform to all requirements given in Table 5-7 and may claim such compliance by using the following conformance statement *"This product conforms to the SpaceWire RMAP Read specification of the ECSS SpaceWire Protocols Standard (ECSS-E-ST-50-52)"*.

b. The supplier of the RMAP equipment shall provide a table detailing the read characteristics of the RMAP implementation.

NOTE An example of the required table is given in Table 5-8.

Table 5-7: SpaceWire RMAP Read Command

Relevant clauses	Title
ECSS-E-ST-50-51, Clause 5	Protocol Identifier
5.5	Read Command
5.6	Error Codes

Table 5-8: Example Read Command Product Characteristics

Read Command			
Action	**Supported**	**Maximum Data Length (bytes)**	**Non-aligned access accepted**
8-bit read	No	-	-
16-bit read	No	-	-
32-bit read	Yes	12	No
64-bit read	No	-	-
Word or byte address	Word address 32-bit aligned		
Endian order	Big endian i.e. the most significant byte of the memory location is returned as the first byte		
Accepted logical addresses	0xFE at power-on 0x42 after initialisation		
Target logical address in reply	Logical address of target		
Accepted keys	0x20		
Accepted address ranges	0x00 0000 0000 - 0x00 0000 001C 0x00 0000 0020 - 0x00 0000 003C		
Address incrementation	Incrementing address only		
Status codes returned	All		

5.8.2.5 RMAP Read-Modify-Write Command

a. An implementation of the "SpaceWire RMAP Read-Modify-Write Command" shall conform to all requirements given in Table 5-9 and may claim such compliance by using the following conformance statement *"This product conforms to the SpaceWire RMAP Read-Modify-Write specification of the ECSS SpaceWire Protocols Standard (ECSS-E-ST-50-52)"*.

b. The supplier of the RMAP equipment should provide a table detailing the characteristics of the RMAP implementation.

NOTE An example of the required table is given in Table 5-10.

Table 5-9: SpaceWire RMAP Read-Modify-Write Command

Relevant clauses	Title
ECSS-E-ST-50-51, Clause 5	Protocol Identifier
5.4	Read-Modify-Write Command
5.6	Error Codes

Table 5-10: Example Read-Modify-Write Command Product Characteristics

Read-Modify-Write Command			
Action	**Supported**	**Maximum Data Length (bytes)**	**Non-aligned access accepted**
8-bit read-modify-write	No	-	-
16-bit read-modify-write	No	-	-
32-bit read-modify-write	Yes	4	No
64-bit read-modify-write	No	-	-
Word or byte address	Byte address 32-bit word		
Endian order	Little endian i.e. first byte received goes in least significant byte of memory location		
Accepted logical addresses	0xFE at power-on 0x42 after initialisation		
Target logical address in reply	What was in command		
Accepted keys	0x20		
Accepted address ranges	0x00 0000 0000 - 0x00 0000 001C		
Status codes returned	All		

Annex A (informative) Example of RMAP CRC implementation

A.1 Overview

In this example implementations of the CRC used by RMAP are provide in VHDL and C-code.

A.2 VHDL implementation of RMAP CRC

```
-----------------------------------------------------------------------------
   -- Cyclic Redundancy Code (CRC) for Remote Memory Access Protocol (RMAP)
   -----------------------------------------------------------------------------
   -- Purpose:
   --    Given an intermediate SpaceWire RMAP CRC byte value and an RMAP header
   --    or data byte, return an updated RMAP CRC byte value.
   --
   -- Parameters:
   --    INCRC(7:0)  - The intermediate  RMAP CRC byte value.
   --    INBYTE(7:0) - The RMAP Header or Data byte.
   --
   -- Return value:
   --    OUTCRC(7:0) - The updated RMAP CRC byte value.
   --
   -- Description:
   --    One-to-many implementation: Galois version of LFSR (reverse CRC).
   --
   --         +---+   +---+   +---+   +---+   +---+   +---+    +---+    +---+
   -- out <-+-| 7 |<--| 6 |<--| 5 |<--| 4 |<--| 3 |<--| 2 |<-X-| 1 |<-X-| 0 |<-+
   --       | +---+   +---+   +---+   +---+   +---+   +---+  ^ +---+  ^ +---+  |
   --       |                                                |        |        |
   --       v                                                |        |        |
   -- in -->X------------------------------------------------+--------+--------+
   --    x**8     x**7    x**6    x**5    x**4    x**3    x**2     x**1     x**0
   --
   --    Generator polynomial: g(x) = x**8 + x**2 + x**1 + x**0
   --
   -- Notes:
   --    The INCRC input CRC value must have all bits zero for the first INBYTE.
   --
```

```
--    The first INBYTE must be the first Header or Data byte covered by the
--    RMAP CRC calculation. The remaining bytes must be supplied in the RMAP
--    transmission/reception byte order.
--
--    If the last INBYTE is the last Header or Data byte covered by the RMAP
--    CRC calculation then the OUTCRC output will be the RMAP CRC byte to be
--    used for transmission or to be checked against the received CRC byte.
--
--    If the last INBYTE is the Header or Data CRC byte then the OUTCRC
--    output will be zero if no errors have been detected and non-zero if
--    an error has been detected.
--
--    Each byte is inserted in or extracted from a SpaceWire packet without
--    the need for any bit reversal or similar manipulation. The SpaceWire
--    packet transmission and reception procedure does the necessary bit
--    ordering when sending and receiving Data Characters (see ECSS-E-ST-50-12).
--
--    SpaceWire data is sent/received Least Significant Bit (LSB) first:
--       INBYTE(0) is the LSB of SpaceWire data byte (sent/received first)
--       INCRC(0)  is the LSB of SpaceWire data byte (sent/received first)
--
--------------------------------------------------------------------------------
function RMAP_CalculateCRC (
   constant INCRC:   in Std_Logic_Vector(7 downto 0);
   constant INBYTE:  in Std_Logic_Vector(7 downto 0))
   return               Std_Logic_Vector is  -- Same range as the two inputs

   -- This variable is to hold the output CRC value.
   variable OUTCRC:     Std_Logic_Vector(7 downto 0);

   -- Internal Linear Feedback Shift Register (LFSR). Note that the
   -- vector indices correspond to the powers of the Galois field
   -- polynomial g(x) which are NOT the same as the indices of the
   -- SpaceWire data byte.
   variable LFSR:       Std_Logic_Vector(7 downto 0);
begin
   -- External to internal bit-order reversal to match indices.
   for i in 0 to 7 loop
      LFSR(7-i) := INCRC(i);
   end loop;

   -- Left shift LFSR eight times feeding in INBYTE bit 0 first (LSB).
   for j in 0 to 7 loop
      LFSR(7 downto 0) := (LFSR(6 downto 2)) &
                          (INBYTE(j) xor LFSR(7) xor LFSR(1)) &
                          (INBYTE(j) xor LFSR(7) xor LFSR(0)) &
                          (INBYTE(j) xor LFSR(7));
   end loop;

   -- Internal to external bit-order reversal to match indices.
```

```
      for i in 0 to 7 loop
         OUTCRC(7-i) := LFSR(i);
      end loop;

      -- Return the updated RMAP CRC byte value.
      return OUTCRC;
   end function RMAP_CalculateCRC;
```

A.3 C-code implementation of RMAP CRC

```
/*
 * The local look-up table used to calculate the updated RMAP CRC
 * byte from the intermediate CRC byte and the input byte.
 */
static const unsigned char RMAP_CRCTable[] = {
   0x00, 0x91, 0xe3, 0x72, 0x07, 0x96, 0xe4, 0x75,
   0x0e, 0x9f, 0xed, 0x7c, 0x09, 0x98, 0xea, 0x7b,
   0x1c, 0x8d, 0xff, 0x6e, 0x1b, 0x8a, 0xf8, 0x69,
   0x12, 0x83, 0xf1, 0x60, 0x15, 0x84, 0xf6, 0x67,
   0x38, 0xa9, 0xdb, 0x4a, 0x3f, 0xae, 0xdc, 0x4d,
   0x36, 0xa7, 0xd5, 0x44, 0x31, 0xa0, 0xd2, 0x43,
   0x24, 0xb5, 0xc7, 0x56, 0x23, 0xb2, 0xc0, 0x51,
   0x2a, 0xbb, 0xc9, 0x58, 0x2d, 0xbc, 0xce, 0x5f,
   0x70, 0xe1, 0x93, 0x02, 0x77, 0xe6, 0x94, 0x05,
   0x7e, 0xef, 0x9d, 0x0c, 0x79, 0xe8, 0x9a, 0x0b,
   0x6c, 0xfd, 0x8f, 0x1e, 0x6b, 0xfa, 0x88, 0x19,
   0x62, 0xf3, 0x81, 0x10, 0x65, 0xf4, 0x86, 0x17,
   0x48, 0xd9, 0xab, 0x3a, 0x4f, 0xde, 0xac, 0x3d,
   0x46, 0xd7, 0xa5, 0x34, 0x41, 0xd0, 0xa2, 0x33,
   0x54, 0xc5, 0xb7, 0x26, 0x53, 0xc2, 0xb0, 0x21,
   0x5a, 0xcb, 0xb9, 0x28, 0x5d, 0xcc, 0xbe, 0x2f,
   0xe0, 0x71, 0x03, 0x92, 0xe7, 0x76, 0x04, 0x95,
   0xee, 0x7f, 0x0d, 0x9c, 0xe9, 0x78, 0x0a, 0x9b,
   0xfc, 0x6d, 0x1f, 0x8e, 0xfb, 0x6a, 0x18, 0x89,
   0xf2, 0x63, 0x11, 0x80, 0xf5, 0x64, 0x16, 0x87,
   0xd8, 0x49, 0x3b, 0xaa, 0xdf, 0x4e, 0x3c, 0xad,
   0xd6, 0x47, 0x35, 0xa4, 0xd1, 0x40, 0x32, 0xa3,
   0xc4, 0x55, 0x27, 0xb6, 0xc3, 0x52, 0x20, 0xb1,
   0xca, 0x5b, 0x29, 0xb8, 0xcd, 0x5c, 0x2e, 0xbf,
   0x90, 0x01, 0x73, 0xe2, 0x97, 0x06, 0x74, 0xe5,
   0x9e, 0x0f, 0x7d, 0xec, 0x99, 0x08, 0x7a, 0xeb,
   0x8c, 0x1d, 0x6f, 0xfe, 0x8b, 0x1a, 0x68, 0xf9,
   0x82, 0x13, 0x61, 0xf0, 0x85, 0x14, 0x66, 0xf7,
   0xa8, 0x39, 0x4b, 0xda, 0xaf, 0x3e, 0x4c, 0xdd,
   0xa6, 0x37, 0x45, 0xd4, 0xa1, 0x30, 0x42, 0xd3,
   0xb4, 0x25, 0x57, 0xc6, 0xb3, 0x22, 0x50, 0xc1,
   0xba, 0x2b, 0x59, 0xc8, 0xbd, 0x2c, 0x5e, 0xcf
};
```

```
/*
-----------------------------------------------------------------------------
-- Cyclic Redundancy Code (CRC) for Remote Memory Access Protocol (RMAP)
-----------------------------------------------------------------------------
-- Purpose:
--    Given an intermediate SpaceWire RMAP CRC byte value and an RMAP Header
--    or Data byte, return an updated RMAP CRC byte value.
--
-- Parameters:
--    INCRC  - The intermediate RMAP CRC byte value.
--    INBYTE - The RMAP Header or Data byte.
--
-- Return value:
--    OUTCRC - The updated RMAP CRC byte value.
--
-- Description:
--    Table look-up version: uses the XOR of the intermediate CRC byte with the
--    header/data byte to obtain the updated CRC byte from a look-up table.
--
--    Generator polynomial: g(x) = x**8 + x**2 + x**1 + x**0
--
-- Notes:
--    The INCRC input CRC value must have all bits zero for the first INBYTE.
--
--    The first INBYTE must be the first Header or Data byte covered by the
--    RMAP CRC calculation. The remaining bytes must be supplied in the RMAP
--    transmission/reception byte order.
--
--    If the last INBYTE is the last Header or Data byte covered by the RMAP
--    CRC calculation then the OUTCRC output will be the RMAP CRC byte to be
--    used for transmission or to be checked against the received CRC byte.
--
--    If the last INBYTE is the Header or Data CRC byte then the OUTCRC
--    output will be zero if no errors have been detected and non-zero if
--    an error has been detected.
--
--    Each byte is inserted in or extracted from a SpaceWire packet without
--    the need for any bit reversal or similar manipulation. The SpaceWire
--    packet transmission and reception procedure does the necessary bit
--    ordering when sending and receiving Data Characters (see ECSS-E-ST-50-12).
-----------------------------------------------------------------------------
 */
unsigned char RMAP_CalculateCRC(unsigned char INCRC, unsigned char INBYTE)
   { return RMAP_CRCTable[INCRC ^ INBYTE]; }
```

A.4 RMAP CRC test patterns

The following test patterns are based on complete SpaceWire RMAP commands and replies. The data and CRC values are read from top to bottom and are represented as bytes in hexadecimal notation.

Each byte is inserted in a SpaceWire packet without the need for any bit reversal or similar manipulation. The SpaceWire packet transmission and reception procedure does the necessary bit ordering when sending and receiving Data Characters (see ECSS-E-ST-50-12).

Prerequisites:

Writeable and readable memory at location 0xA0000000 through 0xA0000020.

Key:	0x00
Target Logical Address:	0xFE
Initiator Logical Address:	0x67
Target SpaceWire Address:	0x11 0x22 0x33 0x44 0x55 0x66 0x77, or
	0x11 0x22 0x33 0x44
Initiator SpaceWire Address:	0x99 0xAA 0xBB 0xCC 0xDD 0xEE 0x00, or
	0x99 0xAA 0xBB 0xCC

Note that when the number of bytes in an Initiator SpaceWire Address is not divisible by four, it requires that the Reply SpaceWire Address field in the corresponding command is padded with one or more leading bytes with value 0x00.

-- RMAP non-verified incrementing write-with-reply command - without SpaceWire addresses:

Target Logical Address:	0xFE
Protocol Identifier:	0x01
Instruction:	0x6C
Key:	0x00
Initiator Logical Address:	0x67
Transaction Identifier MS:	0x00
Transaction Identifier LS:	0x00
Extended Address:	0x00
Address MS:	0xA0
Address:	0x00
Address:	0x00
Address LS:	0x00
Data Length MS:	0x00
Data Length:	0x00

Data Length LS:	0x10
Header CRC:	0x9F
Data:	0x01
Data:	0x23
Data:	0x45
Data:	0x67
Data:	0x89
Data:	0xAB
Data:	0xCD
Data:	0xEF
Data:	0x10
Data:	0x11
Data:	0x12
Data:	0x13
Data:	0x14
Data:	0x15
Data:	0x16
Data:	0x17
Data CRC:	0x56

-- Expected RMAP successful write reply to previous command - without SpaceWire addresses:

Initiator Logical Address:	0x67
Protocol Identifier:	0x01
Instruction:	0x2C
Status:	0x00
Target Logical Address:	0xFE
Transaction Identifier MS:	0x00
Transaction Identifier MS:	0x00
Header CRC:	0xED

-- RMAP incrementing read command - without SpaceWire addresses:

Target Logical Address:	0xFE
Protocol Identifier:	0x01
Instruction:	0x4C

Key:	0x00
Initiator Logical Address:	0x67
Transaction Identifier MS:	0x00
Transaction Identifier LS:	0x01
Extended Address:	0x00
Address MS:	0xA0
Address:	0x00
Address:	0x00
Address LS:	0x00
Data Length MS:	0x00
Data Length:	0x00
Data Length LS:	0x10
Header CRC:	0xC9

-- Expected RMAP successful read reply to previous command - without SpaceWire addresses:

Initiator Logical Address:	0x67
Protocol Identifier:	0x01
Instruction:	0x0C
Status:	0x00
Target Logical Address:	0xFE
Transaction Identifier MS:	0x00
Transaction Identifier MS:	0x01
Reserved:	0x00
Data Length MS:	0x00
Data Length:	0x00
Data Length LS:	0x10
Header CRC:	0x6D
Data:	0x01
Data:	0x23
Data:	0x45
Data:	0x67
Data:	0x89
Data:	0xAB
Data:	0xCD
Data:	0xEF

Data:	0x10
Data:	0x11
Data:	0x12
Data:	0x13
Data:	0x14
Data:	0x15
Data:	0x16
Data:	0x17
Data CRC:	0x56

-- RMAP non-verified incrementing write-with-reply command - with SpaceWire addresses:

Target SpaceWire Address:	0x11 (not part of Header CRC)
Target SpaceWire Address:	0x22 (not part of Header CRC)
Target SpaceWire Address:	0x33 (not part of Header CRC)
Target SpaceWire Address:	0x44 (not part of Header CRC)
Target SpaceWire Address:	0x55 (not part of Header CRC)
Target SpaceWire Address:	0x66 (not part of Header CRC)
Target SpaceWire Address:	0x77 (not part of Header CRC)
Target Logical Address:	0xFE
Protocol Identifier:	0x01
Instruction:	0x6E
Key:	0x00
Reply SpaceWire Address:	0x00
Reply SpaceWire Address:	0x99
Reply SpaceWire Address:	0xAA
Reply SpaceWire Address:	0xBB
Reply SpaceWire Address:	0xCC
Reply SpaceWire Address:	0xDD
Reply SpaceWire Address:	0xEE
Reply SpaceWire Address:	0x00
Initiator Logical Address:	0x67
Transaction Identifier MS:	0x00
Transaction Identifier LS:	0x02
Extended Address:	0x00

Address MS:	0xA0
Address:	0x00
Address:	0x00
Address LS:	0x10
Data Length MS:	0x00
Data Length:	0x00
Data Length LS:	0x10
Header CRC:	0x7F
Data:	0xA0
Data:	0xA1
Data:	0xA2
Data:	0xA3
Data:	0xA4
Data:	0xA5
Data:	0xA6
Data:	0xA7
Data:	0xA8
Data:	0xA9
Data:	0xAA
Data:	0xAB
Data:	0xAC
Data:	0xAD
Data:	0xAE
Data:	0xAF
Data CRC:	0xB4

-- Expected RMAP successful write reply to the previous command - with SpaceWire addresses:

Reply SpaceWire Address:	0x99 (not part of Header CRC)
Reply SpaceWire Address:	0xAA (not part of Header CRC)
Reply SpaceWire Address:	0xBB (not part of Header CRC)
Reply SpaceWire Address:	0xCC (not part of Header CRC)
Reply SpaceWire Address:	0xDD (not part of Header CRC)
Reply SpaceWire Address:	0xEE (not part of Header CRC)
Reply SpaceWire Address:	0x00 (not part of Header CRC)
Initiator Logical Address:	0x67

Protocol Identifier:	0x01
Instruction:	0x2E
Status:	0x00
Target Logical Address:	0xFE
Transaction Identifier MS:	0x00
Transaction Identifier MS:	0x02
Header CRC:	0x1D

-- RMAP incrementing read command - with SpaceWire addresses:

Target SpaceWire Address:	0x11 (not part of Header CRC)
Target SpaceWire Address:	0x22 (not part of Header CRC)
Target SpaceWire Address:	0x33 (not part of Header CRC)
Target SpaceWire Address:	0x44 (not part of Header CRC)
Target Logical Address:	0xFE
Protocol Identifier:	0x01
Instruction:	0x4D
Key:	0x00
Reply SpaceWire Address:	0x99
Reply SpaceWire Address:	0xAA
Reply SpaceWire Address:	0xBB
Reply SpaceWire Address:	0xCC
Initiator Logical Address:	0x67
Transaction Identifier MS:	0x00
Transaction Identifier LS:	0x03
Extended Address:	0x00
Address MS:	0xA0
Address:	0x00
Address:	0x00
Address LS:	0x10
Data Length MS:	0x00
Data Length:	0x00
Data Length LS:	0x10
Header CRC:	0xF7

-- Expected RMAP successful read reply to the previous command - with SpaceWire addresses:

Reply SpaceWire Address:	0x99 (not part of Header CRC)
Reply SpaceWire Address:	0xAA (not part of Header CRC)
Reply SpaceWire Address:	0xBB (not part of Header CRC)
Reply SpaceWire Address:	0xCC (not part of Header CRC)
Initiator Logical Address:	0x67
Protocol Identifier:	0x01
Instruction:	0x0D
Status:	0x00
Target Logical Address:	0xFE
Transaction Identifier MS:	0x00
Transaction Identifier MS:	0x03
Reserved:	0x00
Data Length MS:	0x00
Data Length:	0x00
Data Length LS:	0x10
Header CRC:	0x52
Data:	0xA0
Data:	0xA1
Data:	0xA2
Data:	0xA3
Data:	0xA4
Data:	0xA5
Data:	0xA6
Data:	0xA7
Data:	0xA8
Data:	0xA9
Data:	0xAA
Data:	0xAB
Data:	0xAC
Data:	0xAD
Data:	0xAE
Data:	0xAF
Data CRC:	0xB4

Annex B (informative) Example Service Interface specification

B.1 Overview

This example describes the service interface for the RMAP protocol. These services are mapped to the sub-network layer of the CCSDS Standard Onboard Interface Services (SOIS) communication stack which is represented in Figure B-1.

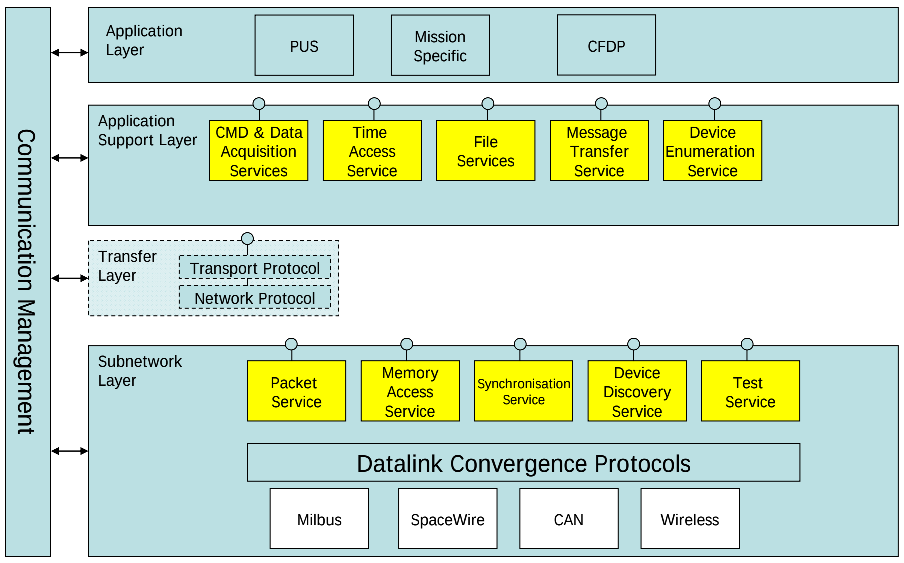


Figure B-1: SOIS communication architecture

The SOIS area of standardisation is intended to provide a common set of communication services which are applicable to all onboard sub-networks. While SOIS defines services it does not define the protocols to implement the services, this is left to expert groups such as ECSS. The services as described in this standard are generally compatible with the SOIS Memory Access Service definition but in some instances the RMAP protocol may include additional capabilities. Annex C provides a detailed mapping between the two sets of services.

B.2 Write Service

B.2.1 Initiator

The service primitives associated with this service are:

a. WRITE.request;

b. WRITE.confirmation.

B.2.2 WRITE.request

B.2.2.1. Function

The RMAP Initiator Write service user passes a WRITE.request primitive to the service provider to request that data is written to memory in a target across the SpaceWire network.

B.2.2.2. Semantics

The WRITE.request primitive provides parameters as follows:

WRITE.request (Target SpaceWire Address, Target Logical Address, Write Command Options, Key, Reply Address, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length, Data)

B.2.2.3. When Generated

The WRITE.request primitive is passed to the RMAP Initiator Write service provider to request it to write the data into memory in the target.

B.2.2.4. Effect On Receipt

Receipt of the WRITE.request primitive causes the RMAP Initiator Write service provider to send an RMAP write command to the target.

B.2.3 WRITE.confirmation

B.2.3.1. Function

The RMAP Initiator Write service provider passes a WRITE.confirmation primitive to the RMAP Initiator Write Service user to confirm that data has been written to memory in a target across the SpaceWire network or to report that an error occurred.

B.2.3.2. Semantics

The WRITE.confirmation primitive provides parameters as follows:

WRITE.confirmation (Transaction Identifier, Status)

B.2.3.3. When Generated

The WRITE.confirmation primitive is passed to the RMAP Initiator Write Service user in the initiator when a write reply is received.

B.2.3.4. Effect On Receipt

The effect on receipt of the WRITE.confirmation primitive on the RMAP Initiator Write Service user in the initiator is undefined.

B.2.3.5. Additional Comments

The transaction identifier parameter is used by the RMAP Initiator Write service user to identify which RMAP transaction is being confirmed.

B.2.4 Target

The service primitives associated with this service are:

a. WRITE.authorisation.indication;

c. WRITE.authorisation.response;

d. WRITE.data.indication;

e. WRITE.data.response;

f. WRITE.indication.

B.2.5 WRITE.authorisation.indication

B.2.5.1. Function

The RMAP Target Write service provider passes a WRITE.authorisation.indication to the RMAP Target Write service user to ask for authorisation to write to memory in the target.

B.2.5.2. Semantics

The WRITE.authorisation.indication primitive provides parameters as follows:

WRITE.authorisation.indication (Target Logical Address, Instruction, Key, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length)

B.2.5.3. When Generated

The WRITE.authorisation.indication primitive is passed from the RMAP Target Write service provider to the RMAP Target Write Service user to request permission to write to memory in the target.

B.2.5.4. Effect On Receipt

The effect of receipt of the WRITE.authorisation.indication primitive on the RMAP Target Write Service user is for it to issue a

WRITE.authorisation.response primitive either authorising or not authorising the memory write operation.

B.2.6 WRITE.authorisation.response

B.2.6.1. Function

The RMAP Target Write service user passes a WRITE.authorisation.response to the RMAP Target Write service provider to give permission or deny permission to write to memory in the target.

B.2.6.2. Semantics

The WRITE.authorisation.response primitive provides parameters as follows:

WRITE.authorisation.response (Authorise)

B.2.6.3. When Generated

The WRITE.authorisation.response primitive is passed from the RMAP Target Write service user to the RMAP Target Write service provider at the target in response to a WRITE.authorisation.indication primitive.

B.2.6.4. Effect On Receipt

The effect of receipt of the WRITE.authorisation.response primitive on the RMAP Target Write service provider is for it to write data to memory if authorisation is given.

B.2.7 WRITE.data.indication

B.2.7.1. Function

The RMAP Target Write service provider passes a WRITE.data.indication to the RMAP Write service user to write data to memory in the target.

B.2.7.2. Semantics

The WRITE.data.indication primitive provides parameters as follows:

WRITE.data.indication (Extended Address, Address, Data Length, Incrementing/Non-incrementing, Data)

B.2.7.3. When Generated

The WRITE.data.indication primitive is passed from the RMAP Target Write service provider to the RMAP Target Write Service user when permission to write data has been given by the WRITE.authorisation.response primitive.

B.2.7.4. Effect On Receipt

The effect of receipt of the WRITE.data.indication primitive on the RMAP Target Write service user is for data to be written into memory in the target.

B.2.8 WRITE.data.response

B.2.8.1. Function

The RMAP Target Write service user passes a WRITE.data.response to the RMAP Write service provided when data has been written to memory in the target.

B.2.8.2. Semantics

The WRITE.data.response primitive provides parameters as follows:

WRITE.data.response (Status)

B.2.8.3. When Generated

The WRITE.data.response primitive is passed from the RMAP Target Write service user to the RMAP Target Write service provider when data has been successfully written to target memory or a failure has occurred while writing data to target memory by the WRITE.data.indication primitive.

B.2.8.4. Effect On Receipt

The effect of receipt of the WRITE.data.response primitive on the RMAP Target Write service provider is for a reply to be sent to the initiator (or other node) if requested and for a WRITE.indication to be passed to the RMAP Target Write service user.

B.2.9 WRITE.indication

B.2.9.1. Function

The RMAP Target Write service provider passes a WRITE.indication to the RMAP Write service user to indicate that data has been successfully written to memory in the target.

B.2.9.2. Semantics

The WRITE.indication primitive does not have any parameters.

B.2.9.3. When Generated

The WRITE.indication primitive is produced when a WRITE.data.response is received from the RMAP Target Write service user with its status parameter indicating that no fault has occurred during the write operation.

B.2.9.4. Effect On Receipt

The effect of receipt of the WRITE.indication primitive on the RMAP Target Write service user is undefined.

B.3 Read Service

B.3.1 Initiator

The service primitives associated with this service are:

a) READ.request;

b) READ.confirmation.

B.3.2 READ.request

B.3.2.1. Function

The RMAP Initiator Read service user passes a READ.request primitive to the RMAP Initiator Read service provider to request that data is read from memory in a target across the SpaceWire network.

B.3.2.2. Semantics

The READ.request primitive provides parameters as follows:

READ.request (Target SpaceWire Address, Target Logical Address, Read Command Options, Key, Reply Address, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length)

B.3.2.3. When Generated

The READ.request primitive is passed to the RMAP Initiator Read service provider to request it to read data from memory in the target.

B.3.2.4. Effect On Receipt

Receipt of the READ.request primitive causes the RMAP Initiator Read service provider to send an RMAP read command to the target.

B.3.3 READ.confirmation

B.3.3.1. Function

The RMAP Initiator Read service provider passes a READ.confirmation primitive to the RMAP Initiator Read service user to confirm that data has been read from memory in a target across the SpaceWire network and to provide that data to the RMAP Initiator Read service user, or to report that an error occurred.

B.3.3.2. Semantics

The READ.confirmation primitive provides parameters as follows:

READ.confirmation (Transaction Identifier, Status, Data Length, Data)

B.3.3.3. When Generated

The READ.confirmation primitive is passed to the RMAP Initiator Read service user when a read reply is received.

B.3.3.4. Effect On Receipt

The effect on receipt of the READ.confirmation primitive by the RMAP Initiator Read service user is undefined.

B.3.3.5. Additional Comments

The transaction identifier parameter is used by the RMAP Initiator Read service user to identify which RMAP transaction is being confirmed.

B.3.4 Target

The service primitives associated with this service are:

a. READ.authorisation.indication;

g. READ.authorisation.response;

h. READ.data.indication;

i. READ.data.response;

j. READ.indication.

B.3.5 READ.authorisation.indication

B.3.5.1. Function

The RMAP Target Read service provider passes a READ.authorisation.indication to the RMAP Target Read service user to ask for authorisation to read data from memory in the target.

B.3.5.2. Semantics

The READ.authorisation.indication primitive provides parameters as follows:

READ.authorisation indication (Target Logical Address, Instruction, Key, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length)

B.3.5.3. When Generated

The READ.authorisation.indication primitive is passed from the RMAP Target Read service provider to the RMAP Target Read service user at the target to request permission to read data from memory in the target.

B.3.5.4. Effect On Receipt

The effect of receipt of the READ.authorisation.indication primitive on the RMAP Target Read service user is for it to issue a READ.authorisation.response primitive either accepting or rejecting the read operation.

B.3.6 READ.authorisation.response

B.3.6.1. Function

The RMAP Target Read service user passes a READ.authorisation.response to the RMAP Target Read service provider to give permission or deny permission to read from memory in the target.

B.3.6.2. Semantics

The READ.authorisation.response primitive provides parameters as follows:

READ.authorisation.response (Authorise)

B.3.6.3. When Generated

The READ.authorisation.response primitive is passed from the RMAP Target Read service user to the RMAP Target Read service provider at the target in response to a READ.authorisation.indication primitive.

B.3.6.4. Effect On Receipt

The effect of receipt of the READ.authorisation.response primitive on the RMAP Target Read service provider is for it to read data from memory by issuing a READ.data.indication primitive.

B.3.7 READ.data.indication

B.3.7.1. Function

The RMAP Target Read service provider passes a READ.data.indication to the RMAP Target Read service user to read data from memory in the target.

B.3.7.2. Semantics

The READ.data.indication primitive provides parameters as follows:

READ.data.indication (Extended Address, Address, Data Length, Incrementing/Non-incrementing)

B.3.7.3. When Generated

The READ.data.indication primitive is passed from the RMAP Target Read service provider to the RMAP Target Read service user at the target when permission to read data has been given by the READ.authorisation.response primitive.

B.3.7.4. Effect On Receipt

The effect of receipt of the READ.data.indication primitive on the RMAP Target Read service user is for data to be read from memory in the target.

B.3.8 READ.data.response

B.3.8.1. Function

The RMAP Target Read service provider passes a READ.data.response to the RMAP Target Read service user to provide data read from memory in the target.

B.3.8.2. Semantics

The READ.data.response primitive provides parameters as follows:

READ.data.response (Status, Data Length, Data)

B.3.8.3. When Generated

The READ.data.response primitive is passed from the RMAP Target Read service user to the RMAP Target Read service provider after a READ.data.indication has been received.

B.3.8.4. Effect On Receipt

The effect of receipt of the READ.data.response primitive on the RMAP Target Read service provider is for the data read from memory in the target or an error to be returned to the initiator (or other node).

B.3.9 READ.indication

B.3.9.1. Function

The RMAP Target Read service provider passes a READ.indication to the RMAP Target Read service user to indicate that data has been successfully read from memory in the target.

B.3.9.2. Semantics

The READ.indication primitive provides parameters as follows:

READ.indication

B.3.9.3. When Generated

The READ.indication primitive is passed from the RMAP Target Read service provider to the RMAP Target Read service user at the target when data has been successfully read from memory in the target.

B.3.9.4. Effect On Receipt

The effect of receipt of the READ.indication primitive on the RMAP Target Read service user is undefined.

B.4 Read-Modify-Write Service

B.4.1 Initiator

The service primitives associated with this service are:

a. RMW.request;

k. RMW.confirmation.

B.4.2 RMW.request

B.4.2.1. Function

At the initiator, the RMAP read-modify-write service user passes a RMW.request primitive to the service provider to request that data is read-modify-write memory in a target across the SpaceWire network.

B.4.2.2. Semantics

The RMW.request primitive provides parameters as follows:

RMW.request (Target SpaceWire Address, Target Logical Address, RMW Command Options, Key, Reply Address, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length, Data, Mask)

B.4.2.3. When Generated

The RMW.request primitive is passed to the service provider to request it to read-modify-write memory in the target.

B.4.2.4. Effect On Receipt

Receipt of the RMW.request primitive causes the service provider to send an RMAP read-modify-write command.

B.4.3 RMW.confirmation

B.4.3.1. Function

At the initiator, the service provider passes a RMW.confirmation primitive to the RMAP read-modify-write service user to confirm that data has been read-modify-written to memory in the target across the SpaceWire network.

B.4.3.2. Semantics

The RMW.confirmation primitive provides parameters as follows:

RMW.confirmation (Transaction Identifier, Status, Data)

B.4.3.3. When Generated

The RMW.confirmation primitive is passed to the RMAP read-modify-write service user in the initiator when a RMW reply is received to confirm that data has been read-modify-written to the memory in the target and to provide the data read to the initiator.

B.4.3.4. Effect On Receipt

Receipt of the RMW.confirmation primitive by the RMAP read-modify-write service user in the initiator is undefined.

B.4.3.5. Additional Comments

The transaction identifier parameter is used in the initiator to identify which RMAP transaction is being confirmed.

B.4.4 Target

The service primitives associated with this service are:

a. RMW.authorisation.indication;

l. RMW.authorisation.response;

m. RMW.read.data.indication;

n. RMW.read data.response;

o. RMW.write.data.indication;

p. RMW.write.data.response;

q. RMW.indication.

B.4.5 RMW.authorisation.indication

B.4.5.1. Function

The RMAP Target RMW service provider passes a RMW.authorisation.indication to the RMAP Target RMW service user to ask for authorisation to read-modify-write memory in the target.

B.4.5.2. Semantics

The RMW.authorisation.indication primitive provides parameters as follows:

RMW.authorisation.indication (Target Logical Address, Instruction, Key, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length)

B.4.5.3. When Generated

The RMW.authorisation.indication primitive is passed from the RMAP Target RMW service provider to the RMAP Target RMW service user at the target to request permission to read-modify-write memory in the target.

B.4.5.4. Effect On Receipt

The effect of receipt of the RMW.authorisation.indication primitive by the RMAP Target RMW service user is for it to issue a RMW.authorisation.response primitive either accepting or rejecting the RMW operation.

B.4.6 RMW.authorisation.response

B.4.6.1. Function

The RMAP Target RMW service user passes a RMW.authorisation.response to the RMAP Target RMW service provider to give permission or deny permission to read-modify-write memory in the target.

B.4.6.2. Semantics

The RMW.authorisation.response primitive provides parameters as follows:

RMW.authorisation.response (Authorise)

B.4.6.3. When Generated

The RMW.authorisation.response primitive is passed from the RMAP Target RMW service user to the RMAP Target RMW service provider at the target in response to a READ.authorisation primitive.

B.4.6.4. Effect On Receipt

The effect of receipt of the RMW.authorisation.response primitive on the RMAP Target RMW service provider is for it to read memory in the target by issuing a RMW.data.indication primitive.

B.4.7 RMW.read.data.indication

B.4.7.1. Function

The RMAP Target RMW service provider passes a RMW.read.data.indication to the RMAP Target RMW service user to read data from memory in the target.

B.4.7.2. Semantics

The RMW.read.data.indication primitive provides parameters as follows:

RMW.read.data.indication (Extended Address, Address, Data Length, Incrementing/Non-incrementing)

B.4.7.3. When Generated

The RMW.read.data.indication primitive is passed from the RMAP Target RMW service provider to the RMAP Target RMW service user at the target when permission to read data has been given by the RMW.authorisation.response primitive.

B.4.7.4. Effect On Receipt

The effect of receipt of the RMW.read.data.indication primitive on the RMAP Target RMW service user is for data to be read from memory in the target and returned to the service provider in a RMW.read.data.response.

B.4.8 RMW.read.data.response

B.4.8.1. Function

The RMAP Target RMW service provider passes a RMW.read.data.response to the RMAP Target RMW service user to provide data read from memory in the target.

B.4.8.2. Semantics

The READ.data.response primitive provides parameters as follows:

READ.data.response (Status, Data Length, Data)

B.4.8.3. When Generated

The RMW.read.data.response primitive is passed from the RMAP Target RMW service user to the RMAP Target RMW service provider after a RMW.read.data.indication has been received.

B.4.8.4. Effect On Receipt

The effect of receipt of the RMW.read.data.response primitive on the RMAP Target RMW service provider is for the data and mask from the RMW command to be passed to the RMAP RMW service user for combining with the data read from memory and writing back into memory.

B.4.9 RMW.write.data.indication

B.4.9.1. Function

The RMAP Target RMW service provider passes a RMW.write.data.indication to the RMAP RMW service user to modify and write data to memory in the target.

B.4.9.2. Semantics

The RMW.write.data.indication primitive provides parameters as follows:

RMW.write.data.indication (Extended Address, Address, Data Length, Incrementing/Non-incrementing, Data, Mask)

B.4.9.3. When Generated

The RMWwrite.data.indication primitive is passed from the RMAP Target RMW service provider to the RMAP Target RMW Service user when data has been read from memory and returned by the RMW.read.data.response primitive.

B.4.9.4. Effect On Receipt

The effect of receipt of the RMW.write.data.indication primitive on the RMAP Target Write service user is for the data and mask to be combined in some way with the data previously in memory and the new value written into the same memory location in the target.

B.4.10 RMW.write.data.response

B.4.10.1. Function

The RMAP Target RMW service user passes a RMW.write.data.response to the RMAP RMW service provided when data and mask have been combined with the data previously in memory and the new result written to memory in the target.

B.4.10.2. Semantics

The RMW.write.data.response primitive provides parameters as follows:

RMW.write.data.response (Status)

B.4.10.3. When Generated

The RMW.write.data.response primitive is passed from the RMAP Target RMW service user to the RMAP Target RMW service provider when the data and mask have been successfully combined with the data previously in memory and the new result written to target memory or a failure has occurred while combining or writing data to target memory by the RMW.write.data.indication primitive.

B.4.10.4. Effect On Receipt

The effect of receipt of the RMW.write.data.response primitive on the RMAP Target RMW service provider is for a reply to be sent to the initiator (or other node) if requested and for a RMW.indication to be passed to the RMAP Target RMW service user.

B.4.11 RMW.indication

B.4.11.1. Function

At the target, the service provider passes a RMW.indication to the RMAP RMW service user after read-modify-writing memory in the target.

B.4.11.2. Semantics

The RMW.indication primitive does not have any parameters.

B.4.11.3. When Generated

The RMW.indication primitive is produced when a RMW.write.data.response is received from the RMAP Target RMW service user with its status parameter indicating that no fault has occurred during the read-modify-write operation.

B.4.11.4. Effect On Receipt

The effect of receipt of the RMW.indication primitive on the RMAP Target RMW service user is undefined.

Annex C (informative) Mapping to CCSDS SOIS Remote memory access service

The SOIS model for memory access is that there is only one user of that service and that is the user that initiates the service by issuing a WRITE, READ or READ/MODIFY/WRITE request service primitive. This same user is the recipient of the data which derives from a READ request. The memory itself or its manager is not regarded as a user of the service but is a resource embedded in the subnetwork which may be implemented in a number of ways, as can the protocol which accesses it. This provides for an abstract service which is easily implemented at the user end and does not place any unnecessary constraints on the capabilities of the memory being accessed. It does mean, however, that the memory being accessed is part of the subnetwork, not a user of the subnetwork.

The RMAP approach is to provide a conduit where READ, WRITE and READ/MODIFY/WRITE requests and responses are passed transparently through the subnetwork between a user and a memory manager which implements access to the memory. The actual service content provided by the subnetwork is appropriate to a subnetwork providing an encoding for the primitives to be transferred across the subnetwork constraining their utility compared to a simple transparent packet transfer. This allows memory to be accessed which is not embedded in the sub-network, i.e. memory is a user of the subnetwork, not part of the subnetwork.

Figure C-1 and Figure C-2 show the two approaches.

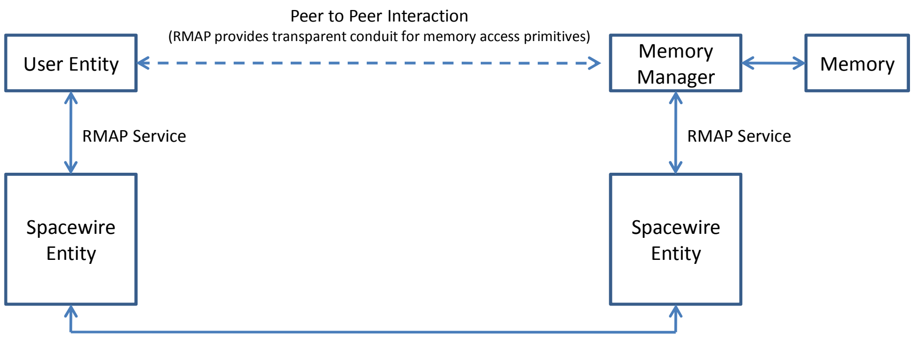


Figure C-1: RMAP model

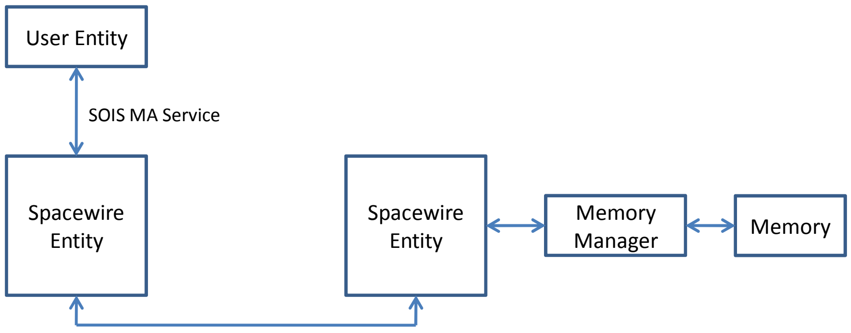


Figure C-2: SOIS model

Note that the RMAP primitives at the memory end have no equivalent in SOIS. Primitives where equivalents do exist have their parameters enumerated. Note also the difference in terminology between confirmations in RMAP and indications in SOIS. This is because the symmetrical nature of RMAP calls for a Request-Indication-Response-Confirm sequence whereas the single ended, asymmetrical nature of the SOIS uses a simple Request-Indication sequence. A Response-Confirm terminology was rejected by SOIS as not reflecting the nature of the interaction at the service interface, which means that memory is a part of the subnetwork and not a user of the subnetwork.

The RMAP READ.confirmation to return the data has a SOIS equivalent in the READ.indication. However the feedback for other requests takes the form of a FAILURE.indication when the user needs to be notified of a failure.

Table C-2 to Table C-7 address equivalence of service parameters for each of the RMAP primitives which have a SOIS equivalent.

The only parameters without equivalents in Table C-2 are the Write Command Options and Key parameters. The connection-orientation implied by the transaction id is not required in SOIS since the explicit details of the memory being accessed is returned in any resulting indication. This is not to say that any implementing protocol can not use the transaction id shorthand, translating to explicit parameters at the service interface.

Table C-3 illustrates this point where the returned MEMORY_ACCESS_FAILURE.indication has explicit memory address details which may have been encoded as a transaction id in the underlying RMAP protocol.

The SOIS parameters in Table C-5 are taken from both the MEMORY_ACCESS_FAILURE.indication and READ.indication primitives.

Table C-1: Comparison of RMAP and Remote Memory Access primitives

RMAP Primitive	SOIS Equivalent
WRITE.request (Target SpaceWire Address, Target Logical Address, Write Command Options, Key, Reply Address, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length, Data)	WRITE.request (SSNSAP, Destination Address, Memory ID, Start Memory Address, Size, Data)
WRITE.confirmation (Transaction Identifier, Status)	MEMORY_ACCESS_FAILURE.indication (SSNSAP, Destination Address, Memory ID, Start Memory Address, Size, Failure Metadata)

WRITE.authorisation.indication;
WRITE.authorisation.response;
WRITE.data.indication;
WRITE.data.response;
WRITE.indication.
	No equivalent

READ.request (Target SpaceWire Address, Target Logical Address, Read Command Options, Key, Reply Address, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length)	READ.request (SSNSAP, Destination Address, Memory ID, Start Memory Address, Size)
READ.confirmation (Transaction Identifier, Status, Data Length, Data)	READ.indication (SSNSAP, Destination Address, Memory ID, Start Memory Address, Size, Data) MEMORY_ACCESS_FAILURE.indication (SSNSAP, Destination Address, Memory ID, Start Memory Address, Size, Failure Metadata)

READ.authorisation.indication;
READ.authorisation.response;
READ.data.indication;
READ.data.response;
READ.indication;
	No equivalent

RMW.request (Target SpaceWire Address, Target Logical Address, RMW Command Options, Key, Reply Address, Initiator Logical Address, Transaction Identifier, Extended Address, Memory Address, Data Length, Data, Mask)	READ/MODIFY/WRITE.request (SSNSAP, Destination Address, Memory ID, Memory Address, Size, Mask, Data)
RMW.confirmation (Transaction Identifier, Status, Data)	MEMORY_ACCESS_FAILURE.indication (SSNSAP, Destination Address, Memory ID, Start Memory Address, Size, Failure Metadata)

RMW.authorisation.indication;
RMW.authorisation.response;
RMW.read.data.indication;
RMW.read data.response;
RMW.write.data.indication;
RMW.write.data.response;
RMW.indication;
	No equivalent

Table C-2: WRITE.request parameters

RMAP Parameters	SOIS Equivalent
Target SpaceWire Address, Target Logical Address	Destination Address
Write Command Options, Key,	
Reply Address, Initiator Logical Address,	SSNSAP
Transaction Identifier	SSNSAP, Destination Address, Memory ID, Start Memory Address, Size
Extended Address, Memory Address	Memory ID, Start Memory Address
Data Length	Size
Data	Data

Table C-3: WRITE.confirmation parameters

RMAP Parameters	SOIS Equivalent
Transaction Identifier	SSNSAP, Destination Address , Memory ID, Start Memory Address, Size
Status	Failure Metadata

Table C-4: READ.request parameters

RMAP Parameters	SOIS Equivalent
Target SpaceWire Address, Target Logical Address	Destination Address
Read Command Options, Key,	
Reply Address, Initiator Logical Address,	SSNSAP
Transaction Identifier	SSNSAP, Destination Address, Memory ID, Start Memory Address, Size
Extended Address, Memory Address	Memory ID, Start Memory Address
Data Length	Size

Table C-5: READ.confirmation parameters

RMAP Parameters	SOIS Equivalent
Transaction Identifier	SSNSAP, Destination Address , Memory ID, Start Memory Address, Size
Status	Failure Metadata
Data Length	Size
Data	Data

Table C-6: RMW.request parameters

RMAP Parameters	SOIS Equivalent
Target SpaceWire Address, Target Logical Address	Destination Address
RMW Command Options, Key,	
Reply Address, Initiator Logical Address,	SSNSAP
Transaction Identifier	SSNSAP, Destination Address, Memory ID, Start Memory Address, Size
Extended Address, Memory Address	Memory ID, Start Memory Address
Data Length	Size
Data	Data
Mask	Mask

Table C-7: RMW.confirmation parameters

RMAP Parameters	SOIS Equivalent
Transaction Identifier	SSNSAP, Destination Address , Memory ID, Start Memory Address, Size
Status	Failure Metadata
	Size
Data	Data

Bibliography

ECSS-ST-S-00	ECSS system - Description, implementation and general requirements
http://www.spacewire.esa.int	SpaceWire website